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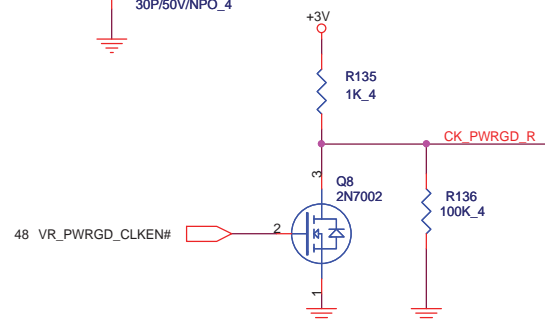
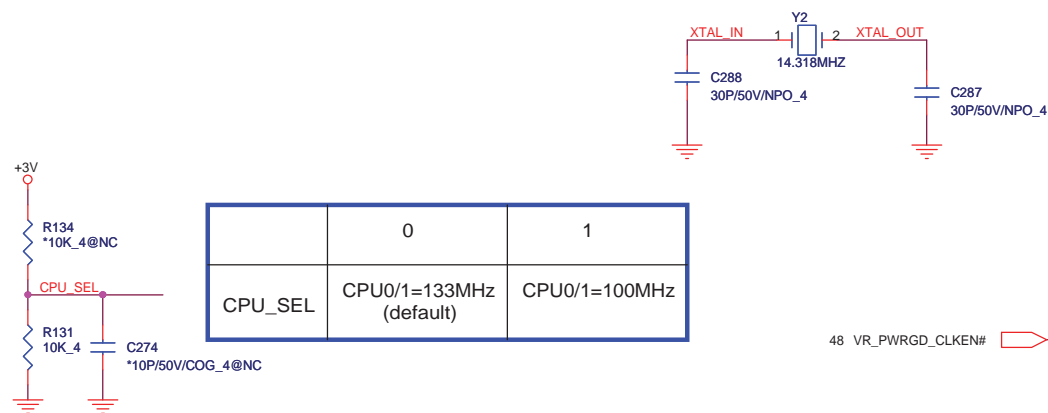
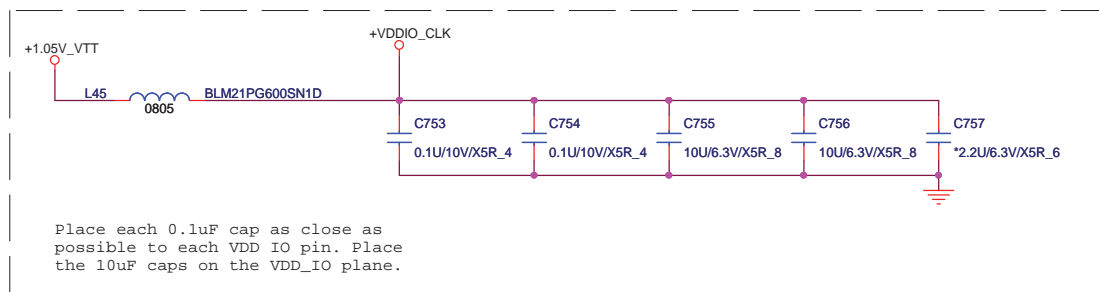
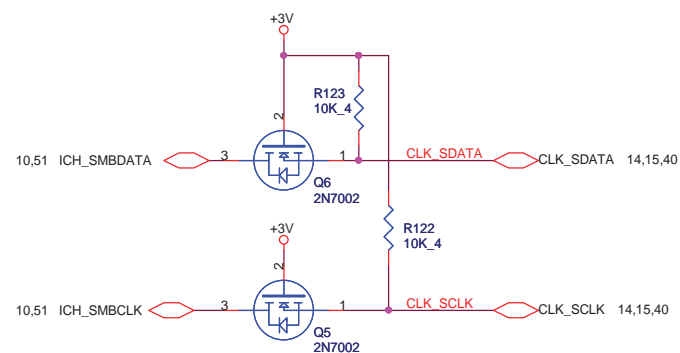
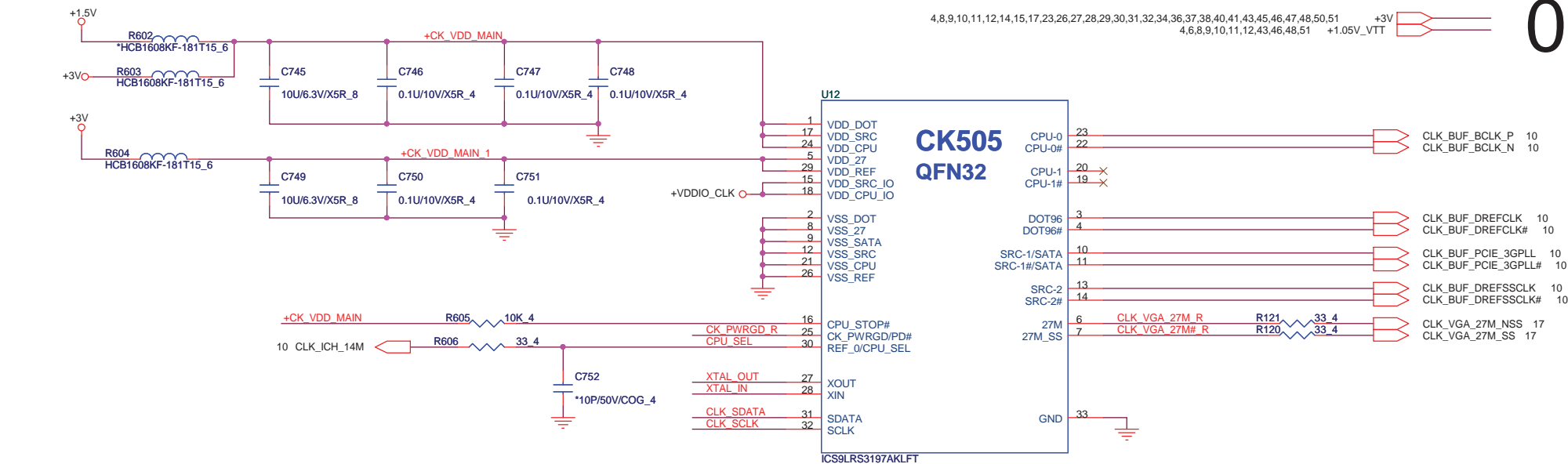
Power States

POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
VIN	10V~+20V	23,32,43,44,45,46,47,48,49,50	MAIN POWER		S0-S5
+3VRTC	+3.0V~+3.3V	9,12,41	RTC		S0-S5
3VPCU	+3.3V	9,23,27,30,32,35,39,41,43,44,47	ITE8052 POWER	3V5V_EN	S0-S5
5VPCU	+5V	14,43,44,45,46,47,49,50	DC/DC POWER IC SOURCE	3V5V_EN	S0-S5
+15V	+15V	23,38,43,45,46,47	LARGE POWER	3V5V_EN	S0-S5
LANVCC	+3.3V	27,43	LAN POWER	LAN_ON	
5V_S5	+5V	12,29,30,43	PCH SUS POWER	S5_ON	S0-S3
3V_S5	+3.3V	8,9,10,11,12,43,52	Sys Management,PCH Resume Well, Intel HD Audio,USB,WLAN WiMAX POWER	S5_ON	S0-S3
5VSUS	+5V	23,39,43,48	SLP_S4# CTRLD POWER	SUSON	S0-S3
3VSUS	+3.3V	14,15,30,34,41,43,49	SLP_S4# CTRLD POWER	SUSON	S0-S3
1.5VSUS	+1.5V	4,6,14,15,43,45,46,49,50	SODIMM POWER	SUSON	S0-S3
0.75VSMDDR_VTERM	+0.75V	14,15,43,45	DDR3 SODIMM REFERENCE POWER	MAIN_ON	S0
+5V	+5V	12,18,23,24,25,26,28,35,37,41,43,44	SLP_S3# CTRLD POWER	MAIN_ON	S0
+3V	+3.3V	3,4,8,9,10,11,12,14,15,17,23,25,26,27,28,29,30,31,32,33,34,36,37,38,39,40,41,43,44,45,46,47,48,50,52	SLP_S3# CTRLD POWER	MAIN_ON	S0
+1.8V	+1.8V	6,12,17,18,21,22,33,43,50	LVDS,NVM POWER	MAIN_ON	S0
+1.5V	+1.5V	12,18,19,20,31,32,34,45,46	Mini PCIe,Express Card POWER	MAIN_ON	S0
+1.05V_VTT	+1.05V	4,6,11,12,43,46,48,52	AuBurndale VTT POWER	MAIN_ON	S0
+1.05V_PCH	+1.05V	3,10,12,43,46,52	PCH CORE POWER	1.05V_RUN_ON	S0
+VCC_GFX_CORE	+0.9V~+1.2V	18,21,43,49	VGA CORE POWER	GFXVR_EN	S0
VCC_CORE		6,43,48	CPU CORE POWER	VRON	S0
LCDVCC	+3.3V	23	LCD Power	ENVDD	S0
+5V_ODD	+5V	28	ODD Power	MAIN_ON	S0
+5V_HDD	+5V	28	HDD Power	MAIN_ON	S0
BAT-V	+10V~+17V	44	MAIN BATTERY	CHG_PBATT	S0-S5



**LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

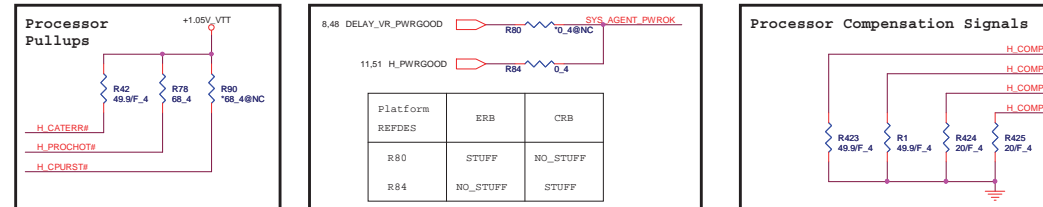
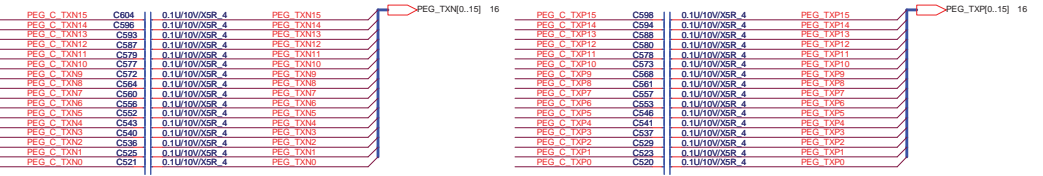
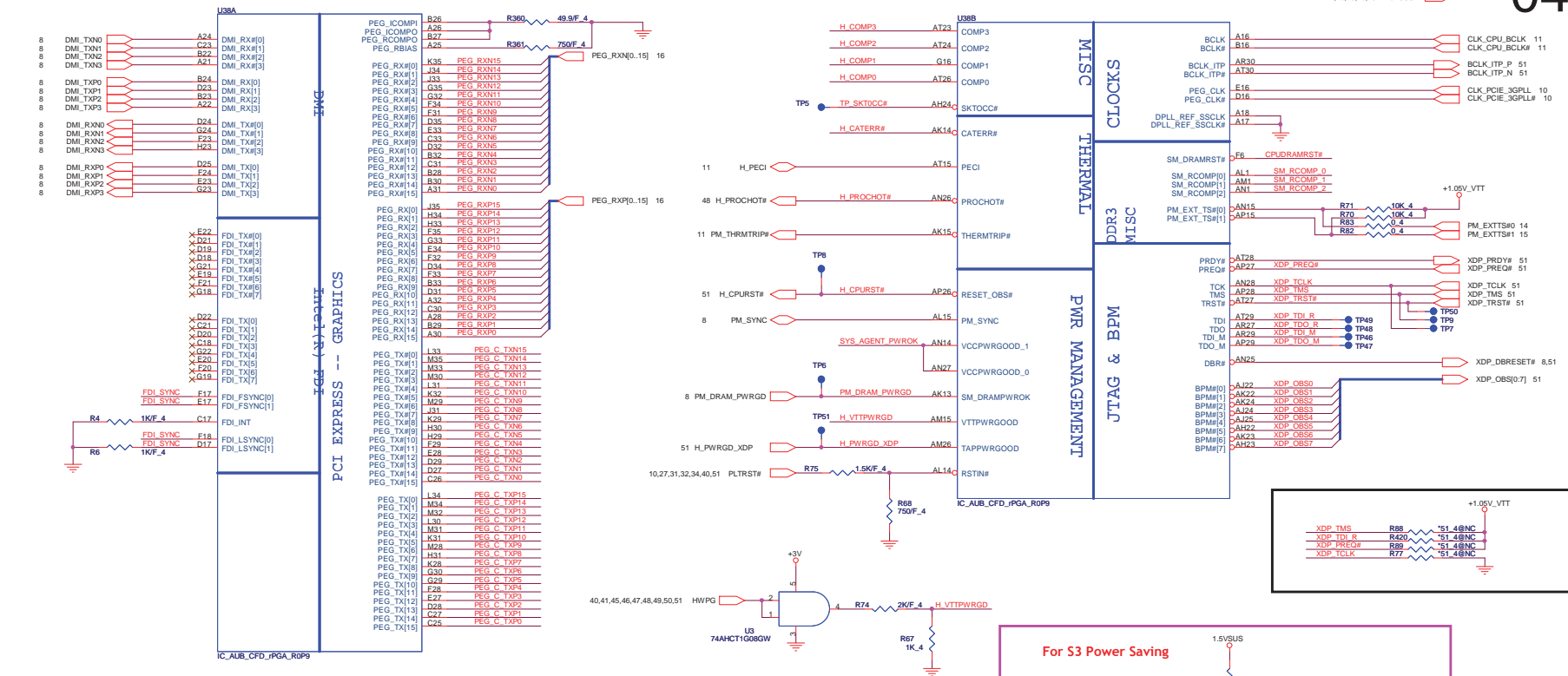
Size A3	Document Number	Index & Power Status	Rev
Date:	Tuesday, November 03, 2009	Sheet 2 of 55	



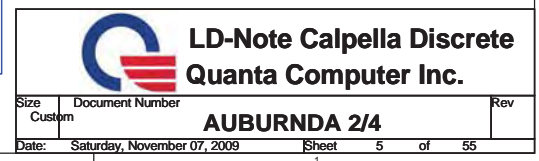
# AUBURNDALE PROCESSOR (DMI, PEG, FDI)

3,8,9,10,11,12,14,15,17,23,26,27,28,29,30,31,32,34,36,37,38,40,41,43,45,46,47,48,50,51  
3,6,8,9,10,11,12,43,46,48,51 +1.05V\_VTT  
14,15,43,45,49 1.5VSUS

04

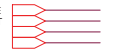


## 05

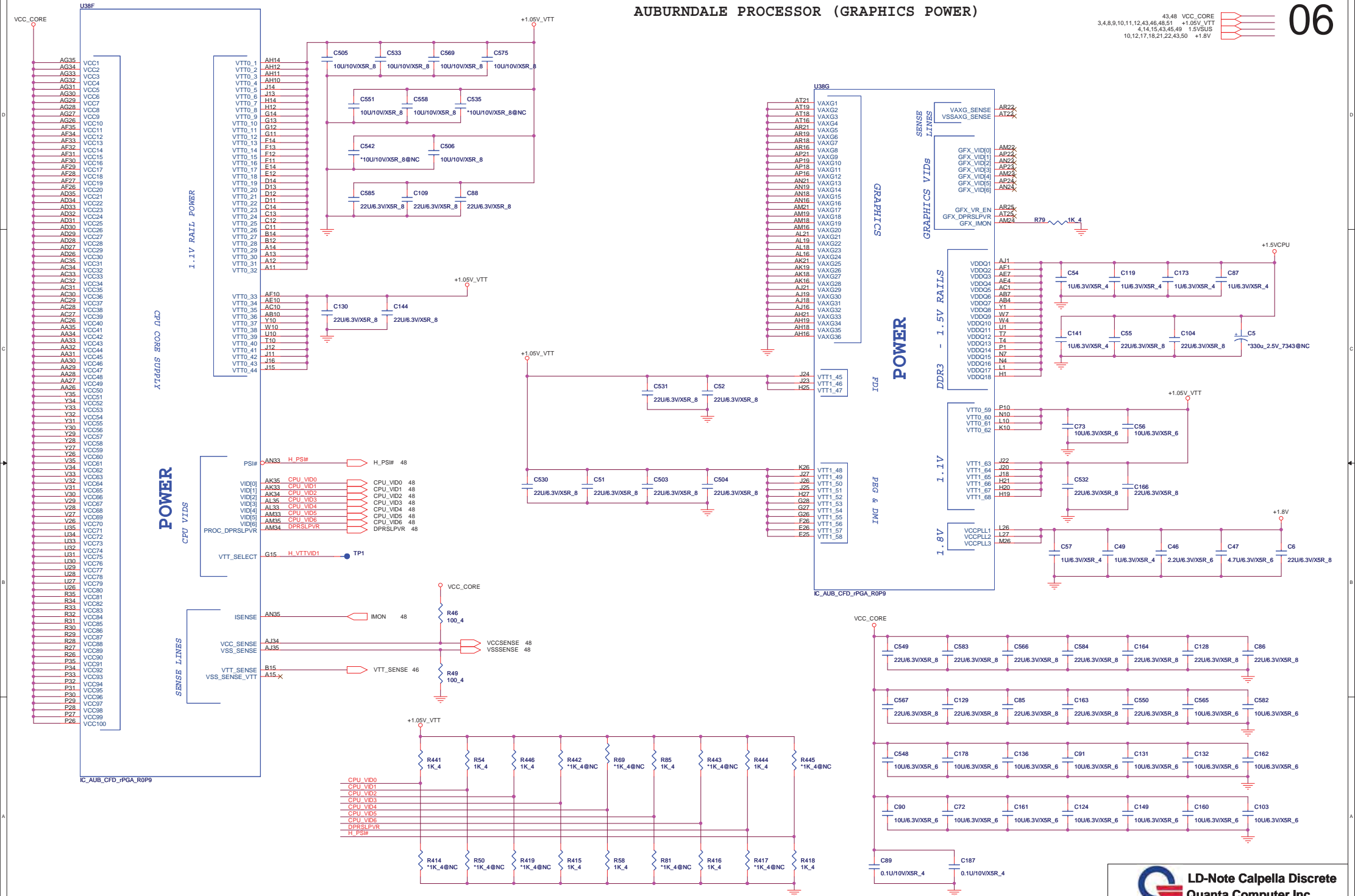


# AUBURNDALE PROCESSOR (GRAPHICS POWER)

43.48 VCC\_CORE  
3.4,8.9,10,11,12,43,46,48,51 +1.05V\_VTT  
4,14,15,43,45,49 1.5VBSUS  
10,12,17,18,21,22,43,50 +1.8V



06

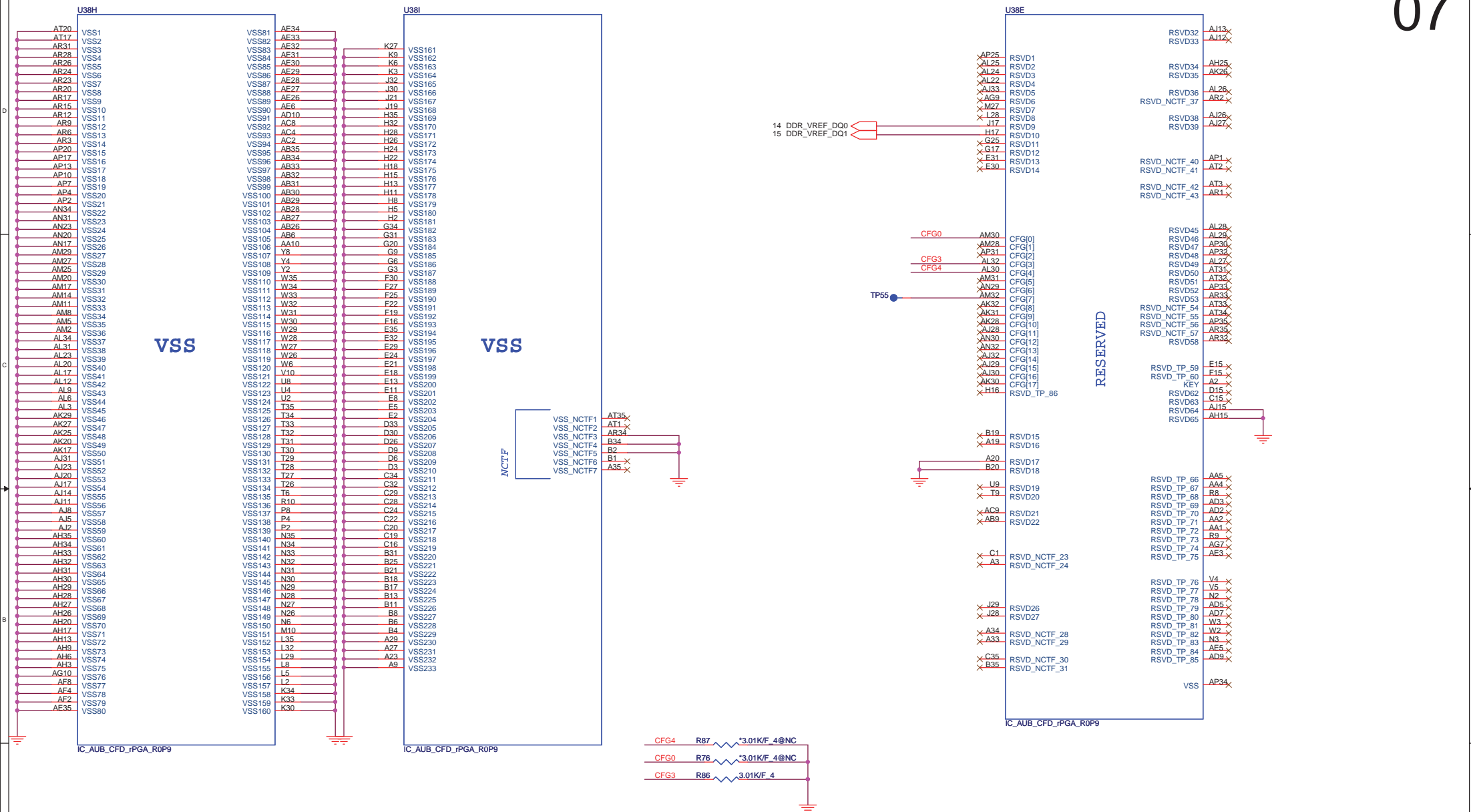




## AUBURNDALDE PROCESSOR (GND)

## AUBURNDALDE PROCESSOR ( RESERVED, CFG)

07

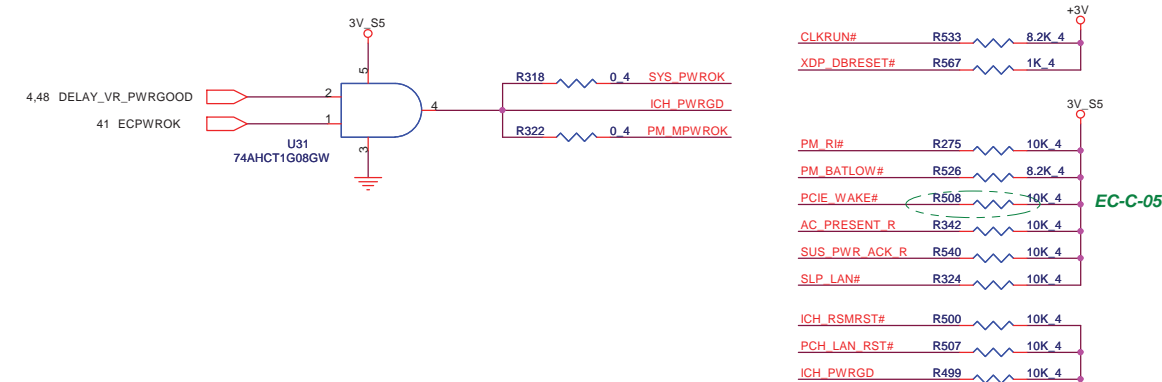
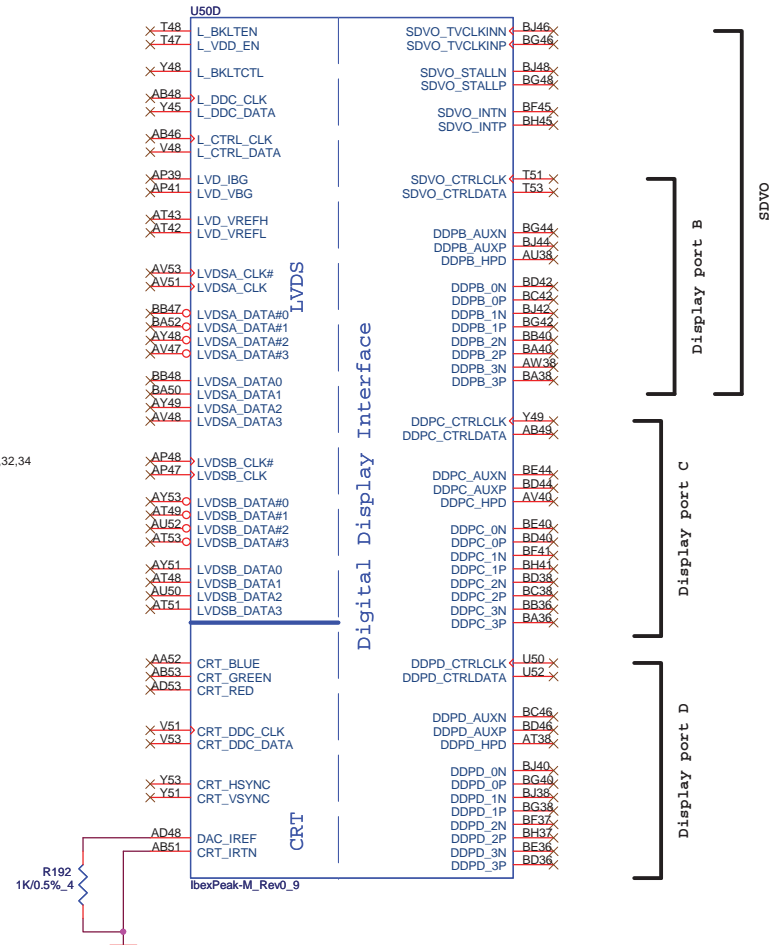
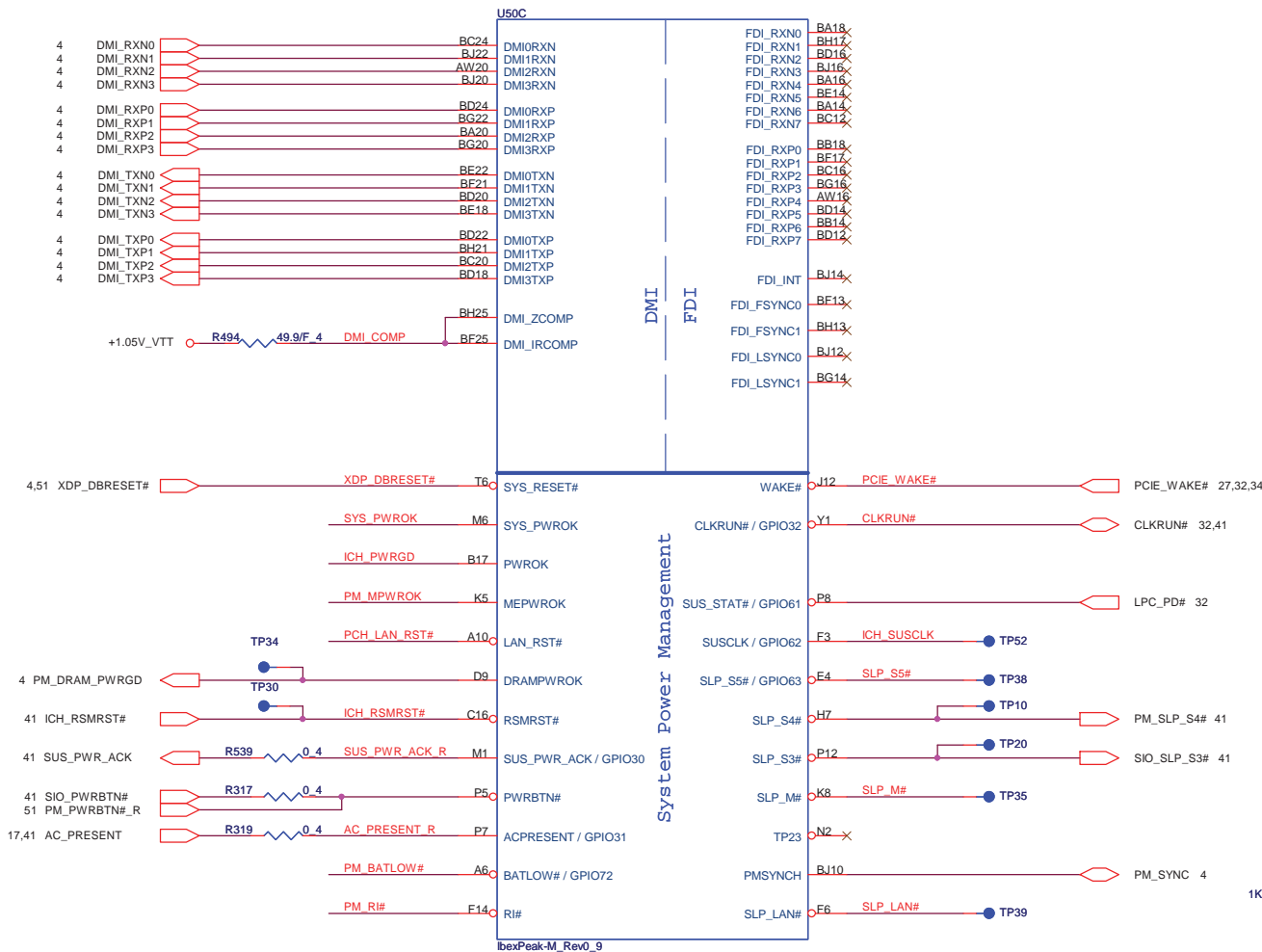


	1	0
CFG4 (Display Port Presence)	Disabled; No Physical Display Port attached to Embedded Display Port	Enabled; An external Display port device is connected to the Embedded Display port
CFG0 (PCI-Epress Configuration Select)	Single PEG	Bifurcation enabled
CFG3 (PCI-Epress Static Lane Reversal)	Normal Operation	Lane Numbers Reversed

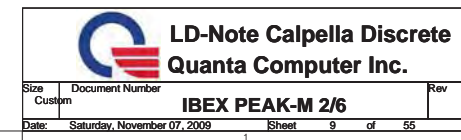
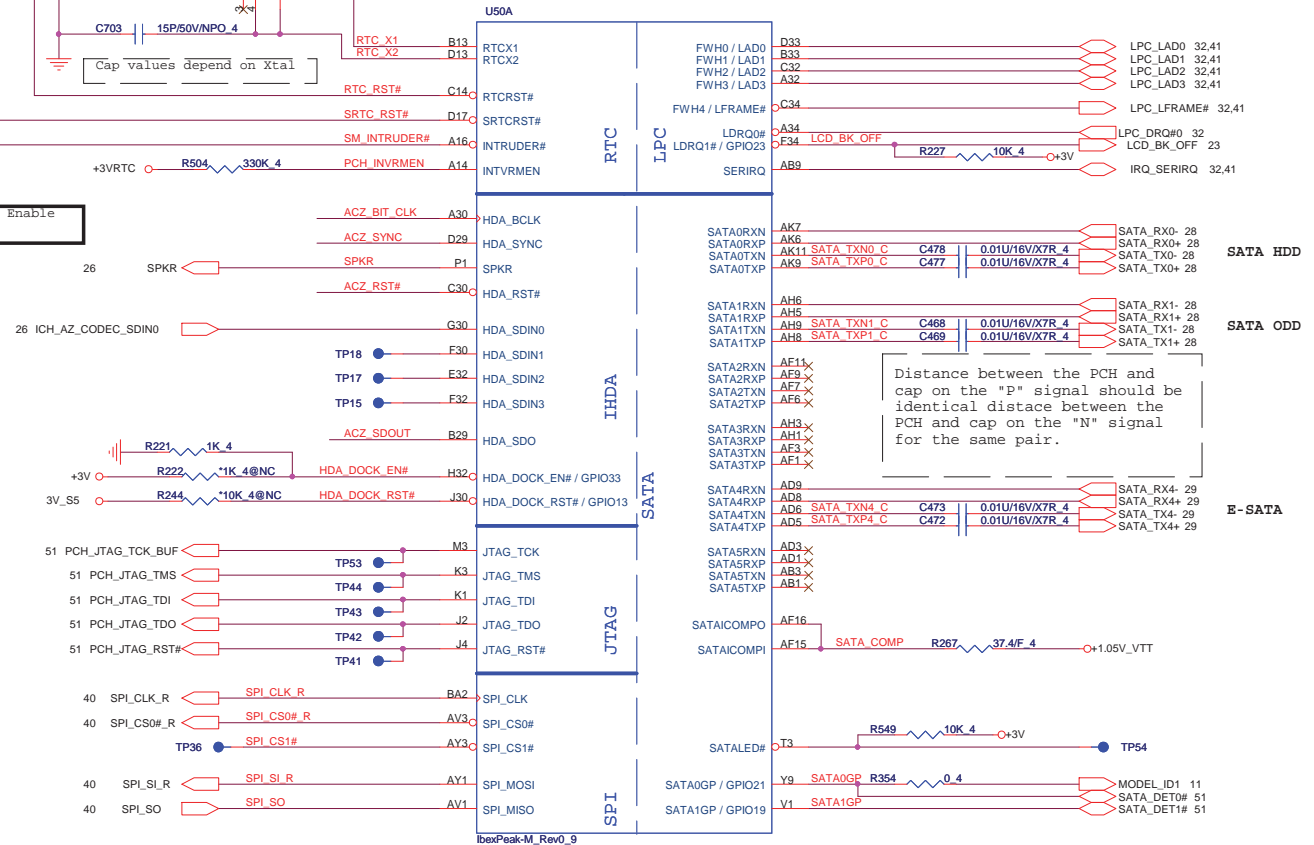
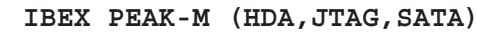


# IBEX PEAK-M (DMI, FDI, GPIO)

# IBEX PEAK-M (LVDS, DDI)



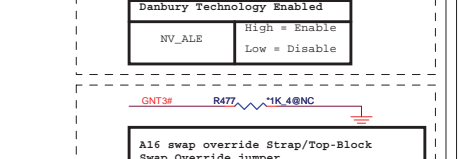
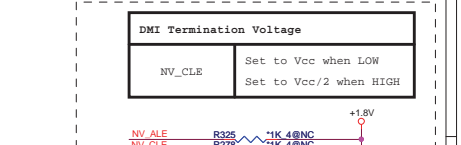
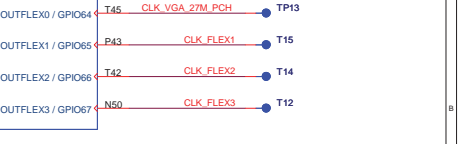
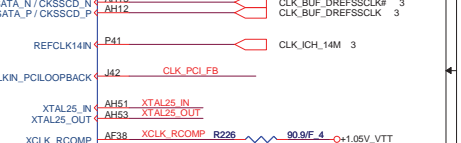
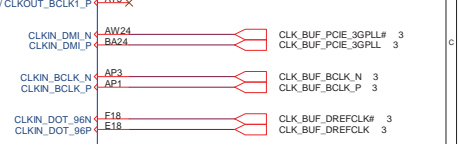
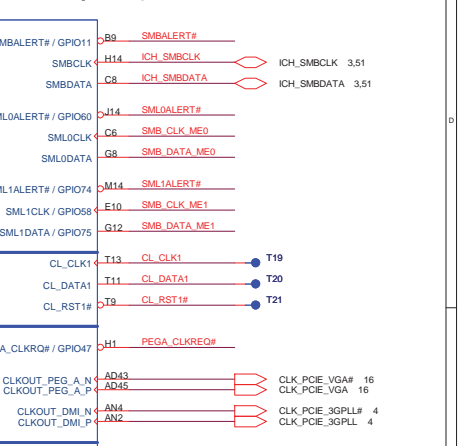
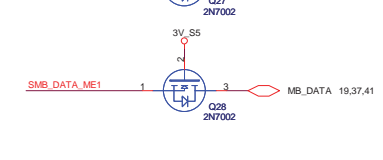
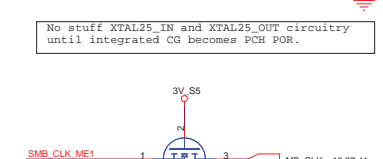
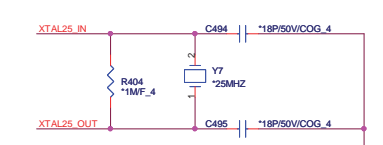
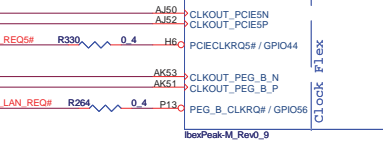
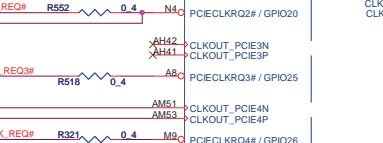
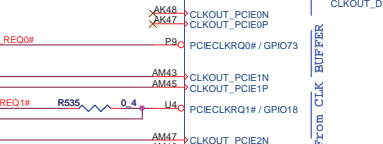
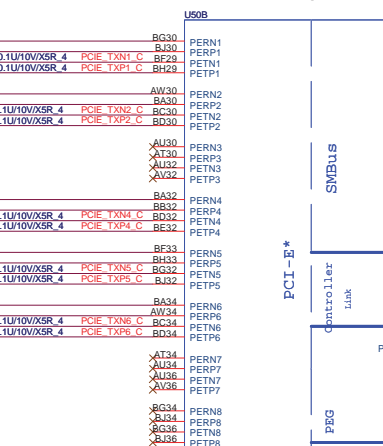
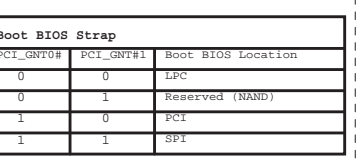
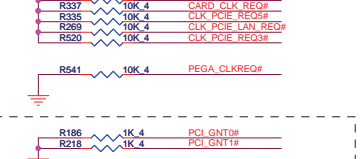
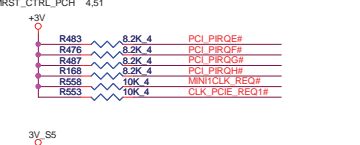
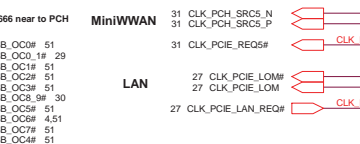
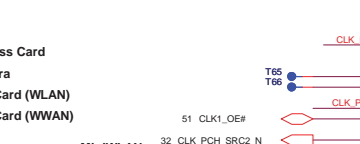
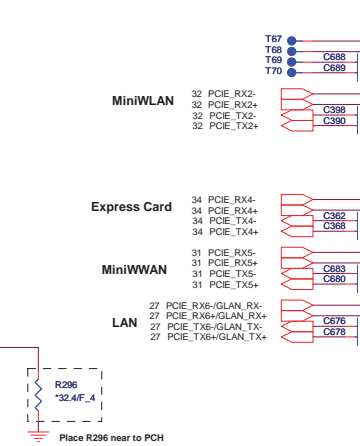
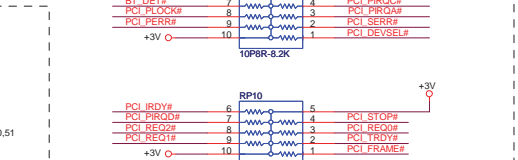
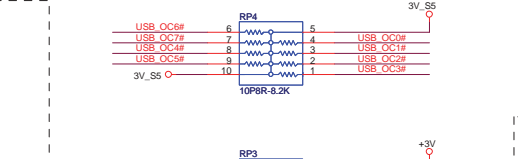
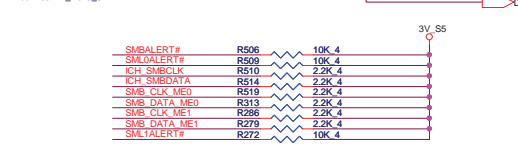
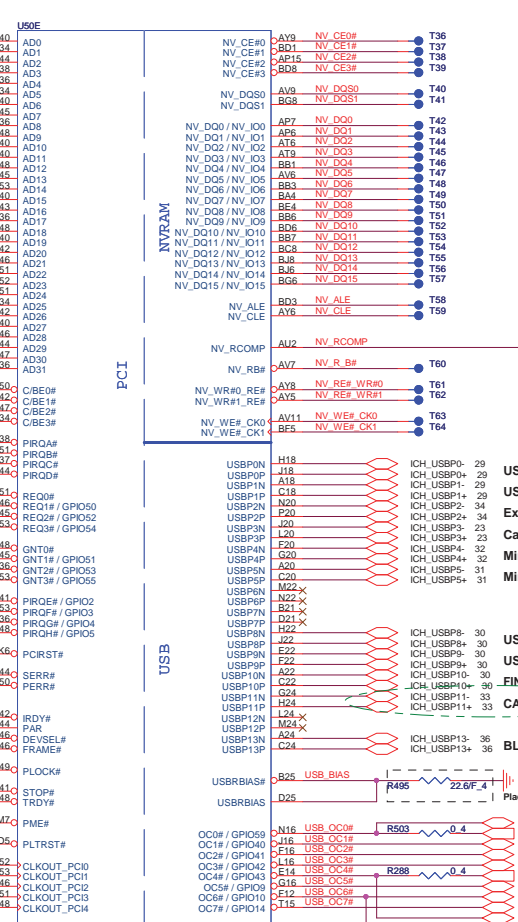
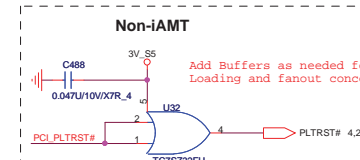
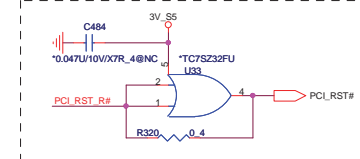
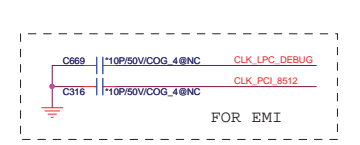
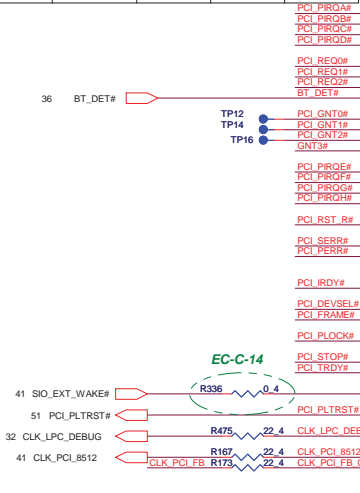




IBEX PEAK-M (PCI,USB,NVRAM)

IBEX PEAK-M (PCI-E,SMBUS,CLK)

USB Port	Device	PM55 (Support)	HM55 (Support)	DV-Stage
Port 0	USB Port	V	V	HM55
Port 1	USB Port	V	V	HM55
Port 2	New Card	V	V	HM55
Port 3	Camera	V	V	HM55
Port 4	WLAN	V	V	HM55
Port 5	WWAN	V	V	HM55
Port 6	Card Reader	V	X	HM55
Port 7	NC	V	X	HM55
Port 8	USB Port	V	V	HM55
Port 9	USB Port	V	V	HM55
Port 10	Finger print	V	V	HM55
Port 11	NC	V	V	HM55
Port 12	NC	V	V	HM55
Port 13	Bluetooth	V	V	HM55



Boot BIOS Strap		
PCI_GNT0#	PCI_GNT1#	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

DMI Termination Voltage	
NV_CLE	Set to Vcc when LOW
NV_CLE	Set to Vcc/2 when HIGH

Danbury Technology Enabled	
NV_ALE	High = Enable
NV_ALE	Low = Disable

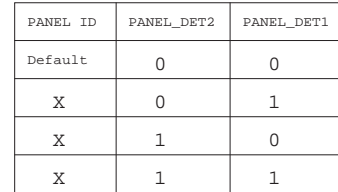
A16 swap override Strap/Top-Block Swap Override jumper	
GNT3#	Low = A16 swap override/Top-Block Swap Override enabled
GNT3#	High = Default




LD-Note Calpella Discrete	
Quanta Computer Inc.	
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3,4,8,9,10,12,14,15,17,23,26,27,28,29,30,31,32,34,36,37,38,40,41,43,45,46,47,48,50,51 +3V  
4,8,9,10,12,29,32,40,43,51 3V\_S5  
3,4,6,8,9,10,12,43,46,48,51 +1.05V\_VTT

PCH GPIO8	R290	10K_4
PCH GPIO12	R512	10K_4
PCH GPIO15	R338	1K_4
TP_PCH_GPIO28	R353	10K_4
PCH_GPIO57	R295	10K_4
CLK_PCIE_REQ6#	R542	10K_4
GPIO46	R528	10K_4
WLAN_OFF#	R511	10K_4

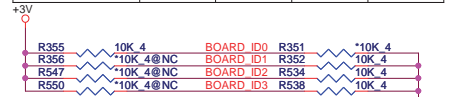
SIO RCIN#	R537	10K 4
SIO A20GATE	R536	10K 4
CCD_ON	R513	10K 4
EXPCRD PWREN#	R230	10K 4
BT_ON#	R316	10K 4
SIO EXT SCI#	R486	10K 4
SIO EXT SM#	R485	10K 4
TEMP ALERT#	R545	10K 4



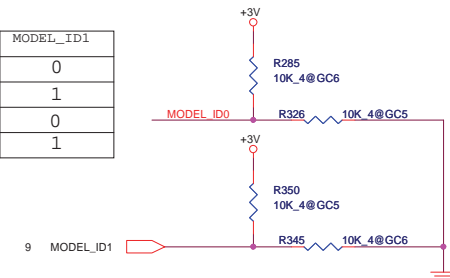
23 PANEL\_DET1  R209  \*10K\_4@NC  PANEL\_DET1#

23 PANEL\_DET2  R341  \*10K\_4@NC  PANEL\_DET2#

Board ID For Function	ID3 GPIO39	ID2 GPIO38	ID1 GPIO37	ID0 GPIO36
SDV	0	0	0	0
SIV	0	0	0	1
SIT	0	0	1	0
SVT	0	0	1	1
SOVP	0	1	0	0



Model ID	MODEL_ID0	MODEL_ID1
13"	0	0
14"	0	1
15"	1	0
Default	1	1

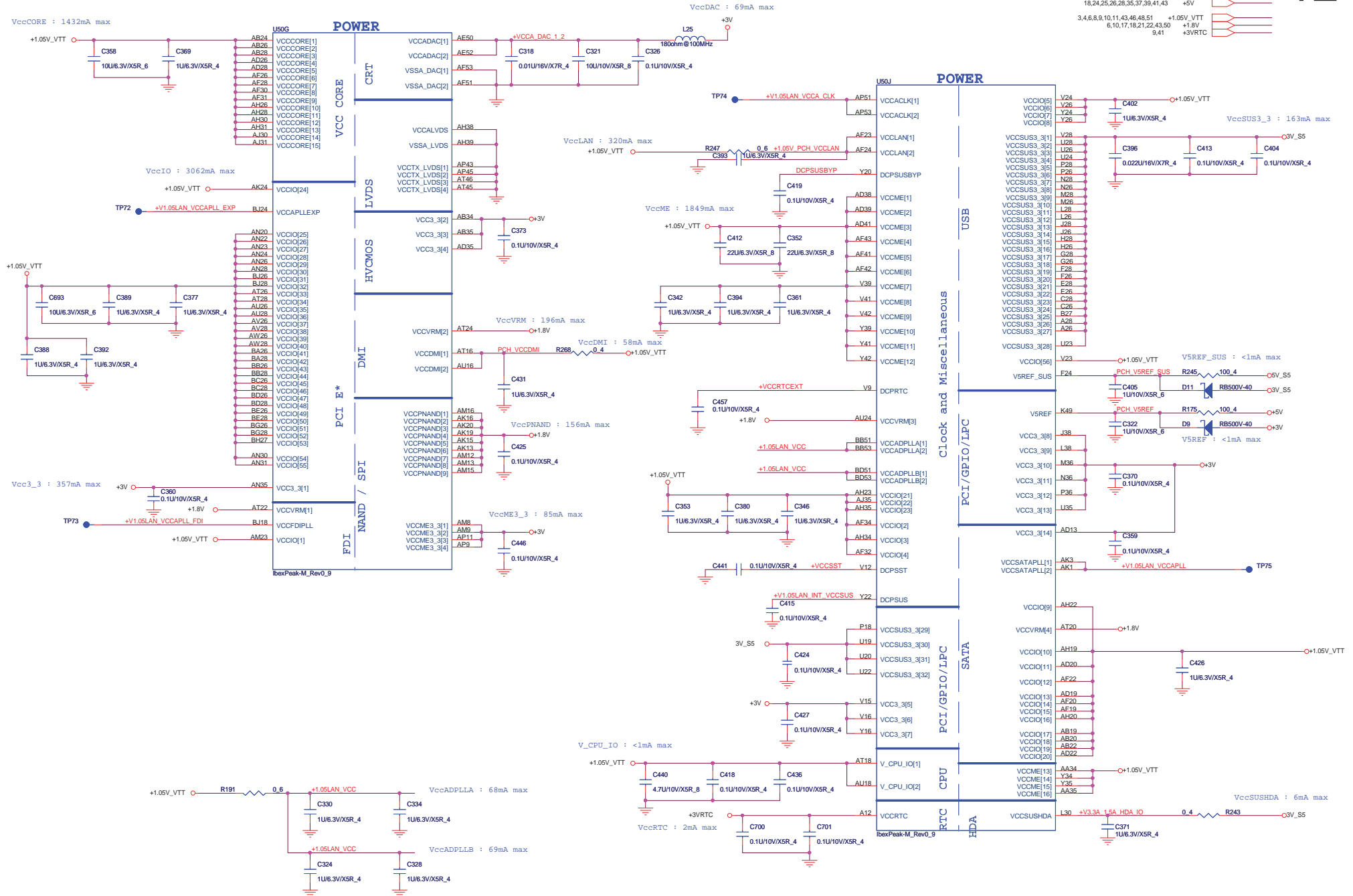


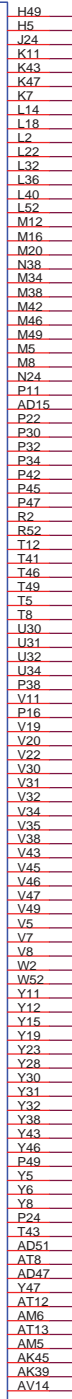
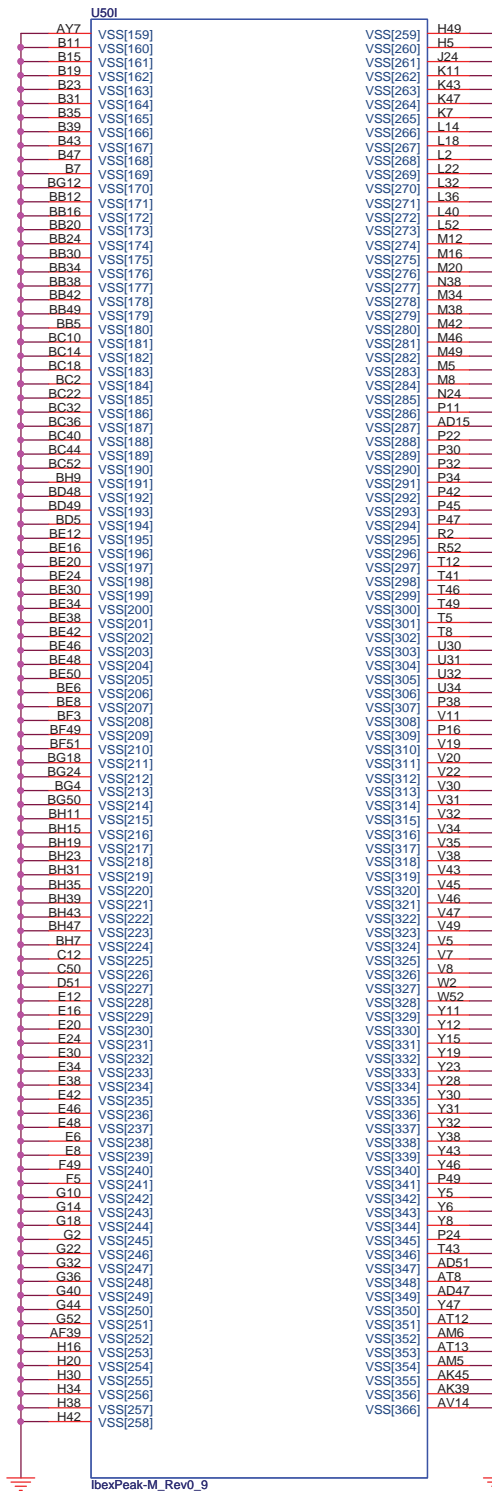
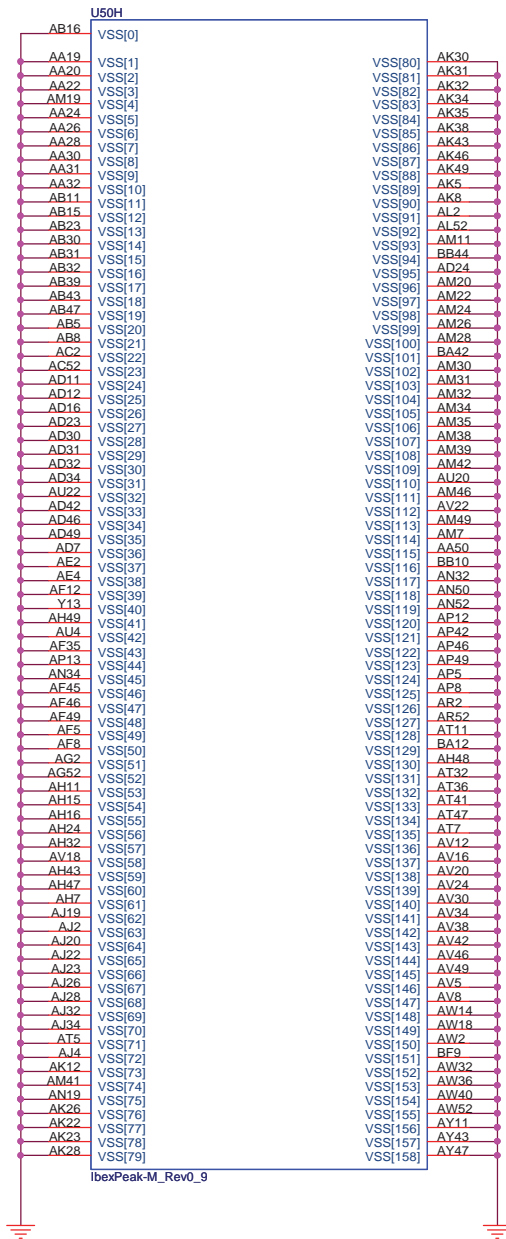
 **LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

Size	Document Number	Rev
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Date:	Sunday, November 08, 2009	Sheet 11 of 55

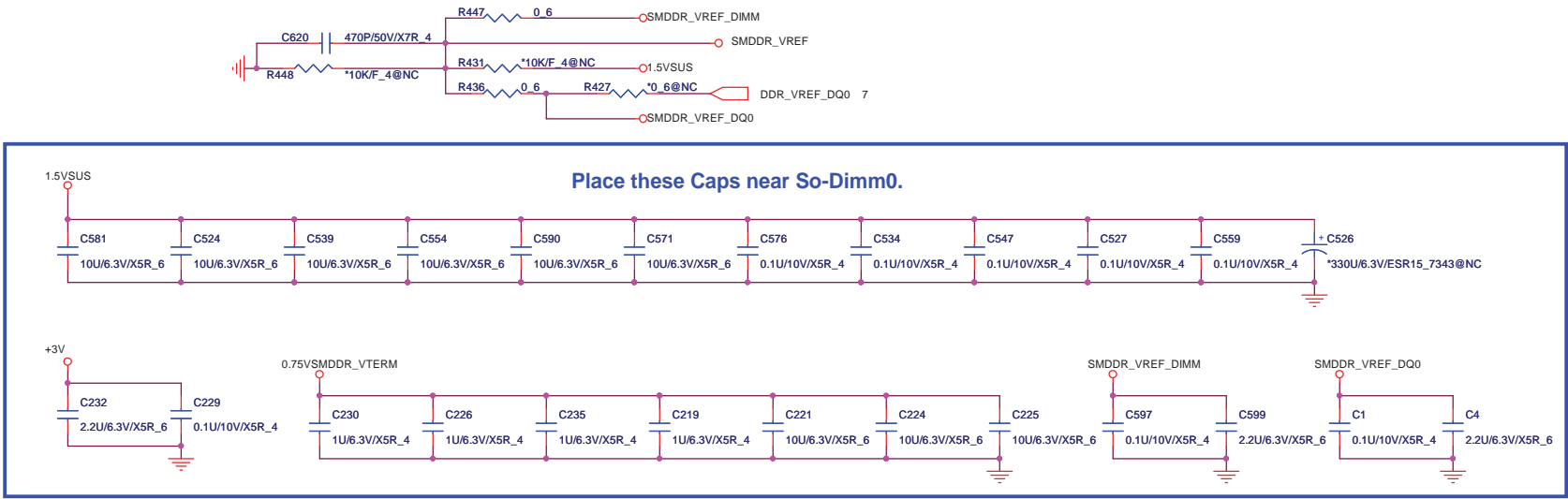
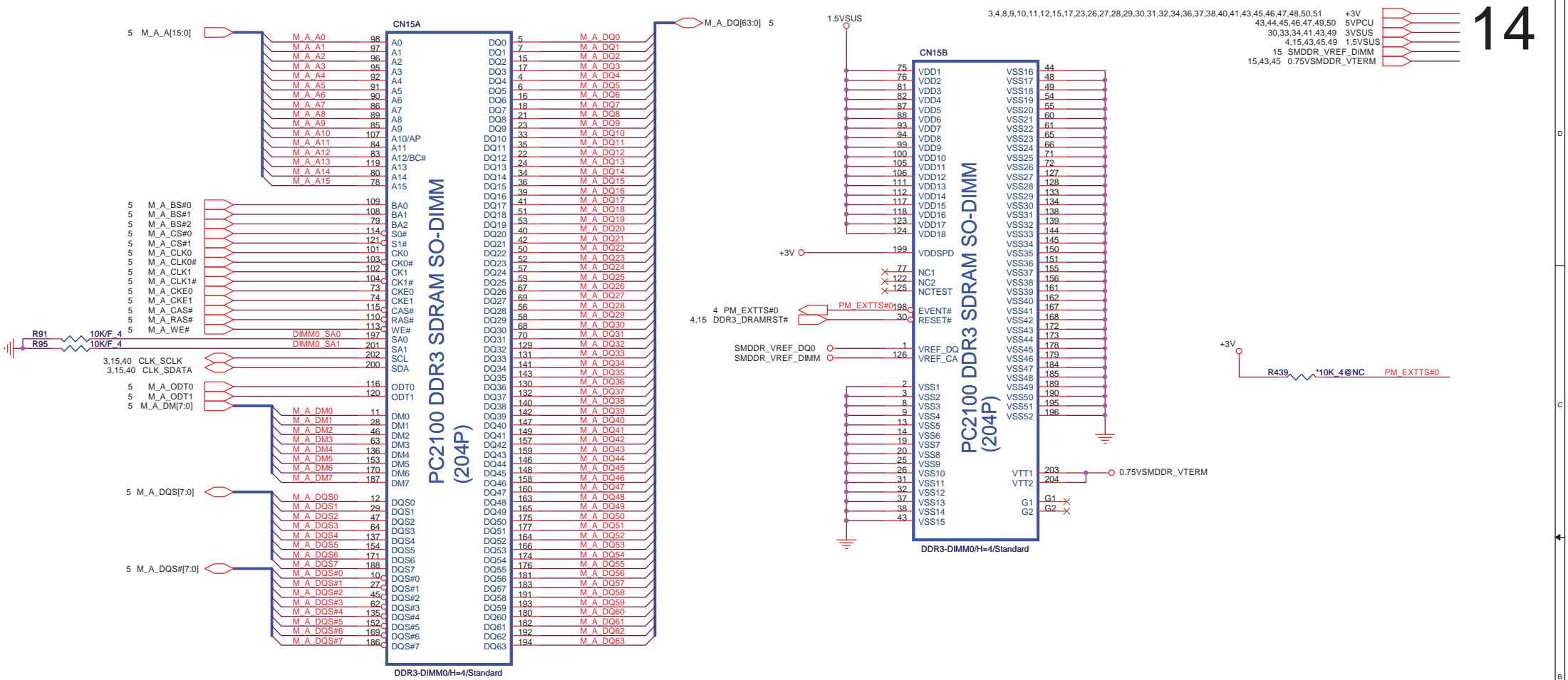
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4,8,9,10,11,23,32,40,43,51  
29,43  
5V\_S5  
+5V  
3,4,6,8,9,10,11,43,46,48,51  
6,10,17,18,21,22,43,50  
+1.06V\_VTT  
+1.8V  
+3VRTC  
9,41


12







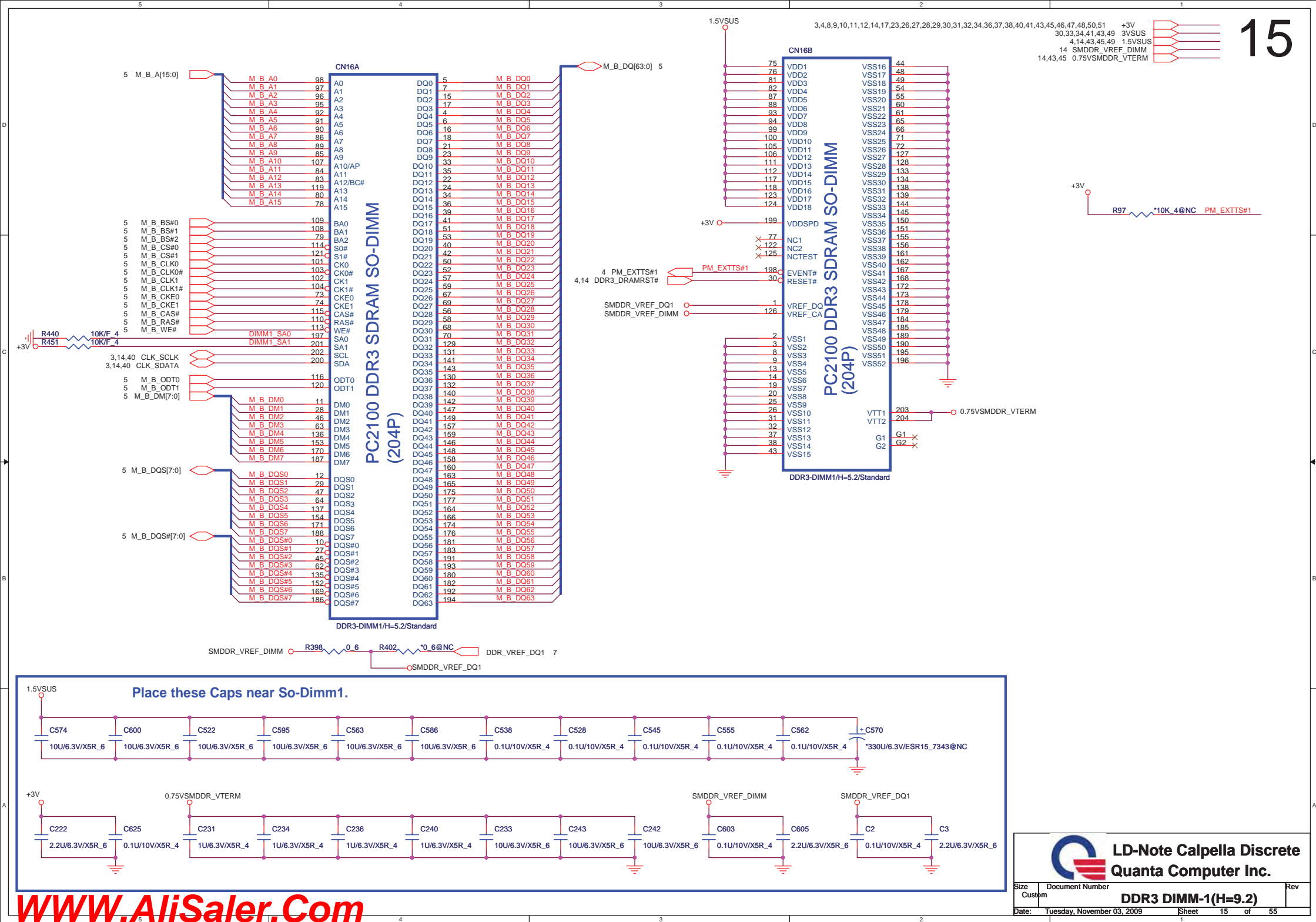




**LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

Size	Document Number	Rev
Custom		
<b>DDR3 DIMM-1(H=5.2)</b>		
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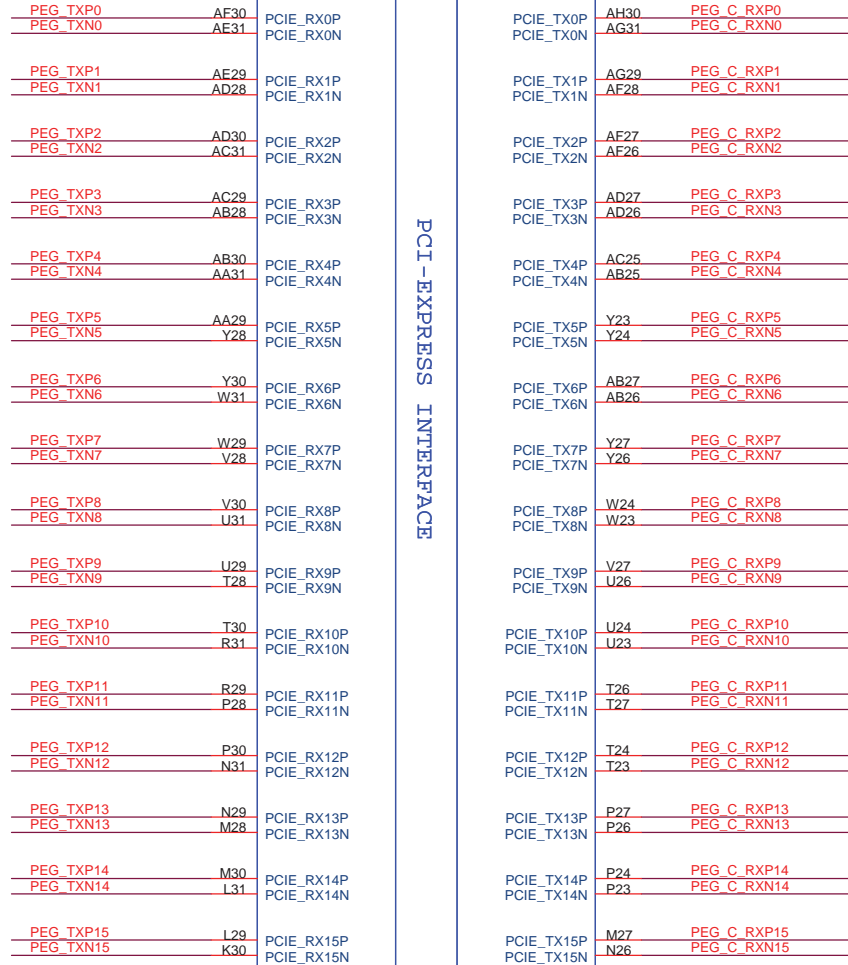
4 PEG\_TXP[0..15]  
4 PEG\_TXN[0..15]



U40A

PART 1 OF 10

PCI-EXPRESS INTERFACE



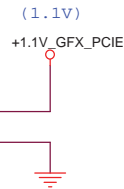
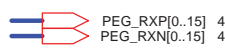
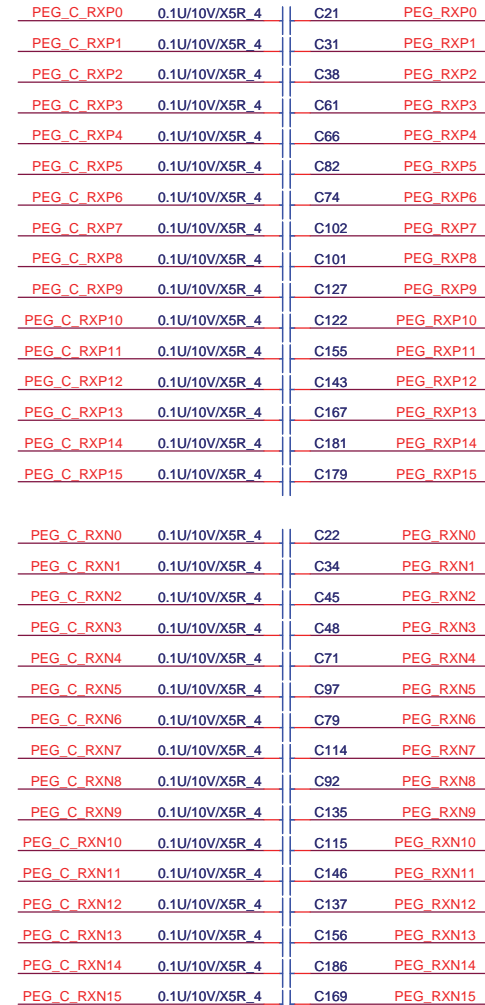
100 MHz (+/-300 ppm) input frequency,  
0-0.7 V single-ended swing.  
clock must be provided less than 400ns  
after CLKREQ# is asserted

M92-S2/M92-XT

18,21,22,43,49 +1.1V\_GFX\_PCIE



16



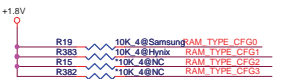
LD-Note Calpella Discrete  
Quanta Computer Inc.

Size Custom	Document Number VGA-M92-XT (PCIe) 1/7	Rev
Date: Saturday, November 07, 2009	Sheet 16 of 55	

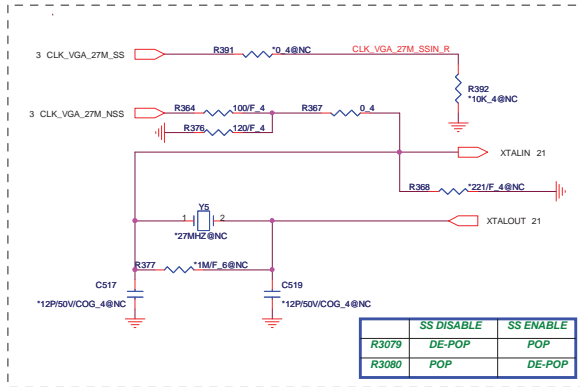
MEMORY APERTURE SIZE SELECT				
MEMORY SIZE	CFG2 GPIO13	CFG1 GPIO12	CFG0 GPIO11	
128MB	0	0	0	
256MB	0	0	1	
64MB	0	1	0	
512MB	1	0	0	

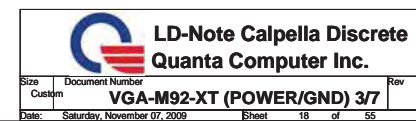


Memory Straps	RAM_TYPE_CFG3	RAM_TYPE_CFG2	RAM_TYPE_CFG1	RAM_TYPE_CFG0
800MHz 512MB(64M*16) Samsung K4W1G1646E-HC12	0	0	0	1
800MHz 512MB(64M*16) Hynix H5T1G63BFR-12C	0	0	1	0



GPIO Straps table	DESCRIPTION OF DEFAULT SETTINGS	G NOTE Setting
GPIO0	GPIO0 - TX_PWRS_ENB (Transmitter Power Savings Enable) 0: 50% Tx output swing for mobile mode 1: full Tx output swing (Default setting for Desktop)	0
GPIO1	GPIO1 - TX_DEEMPH_EN (Transmitter De-emphasis Enable) 0: Tx de-emphasis disabled for mobile mode 1: Tx de-emphasis enabled (Default setting for Desktop)	0
GPIO2	GPIO2 - BIF_GEN2_EN (5.0 Gt/s Enable) 0: Default (Driver Controlled Gen2) 1: Strap Controlled Gen2	0
GPIO3	ATI reserved configuration straps.	0
GPIO4	ATI reserved configuration straps.	0
GPIO5	GPIO 5 - AC_BATT 0: Battery saving mode = 0.0 V 1: AC (Performance mode) = 3.3 V	1
GPIO6	ATI Internal use only	0
GPIO8	ATI reserved configuration straps.	0
GPIO9	VGA Disable 0 - VGA Controller capacity enabled 1 - The device will not recognize as the system's VGA controller.	0
GPIO22	Enable external BIOS ROM device 0 - Disable external BIOS ROM device 1 - Enable external BIOS ROM device	0
HSYNC	AUD[1:0] 00 - No audio function 01 - Audio for DisplayPort and HDMI if adapter is detected 10 - Audio for DisplayPort only 11 - Audio for both DisplayPort and HDMI	1
VSYNC		1

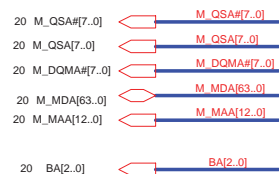
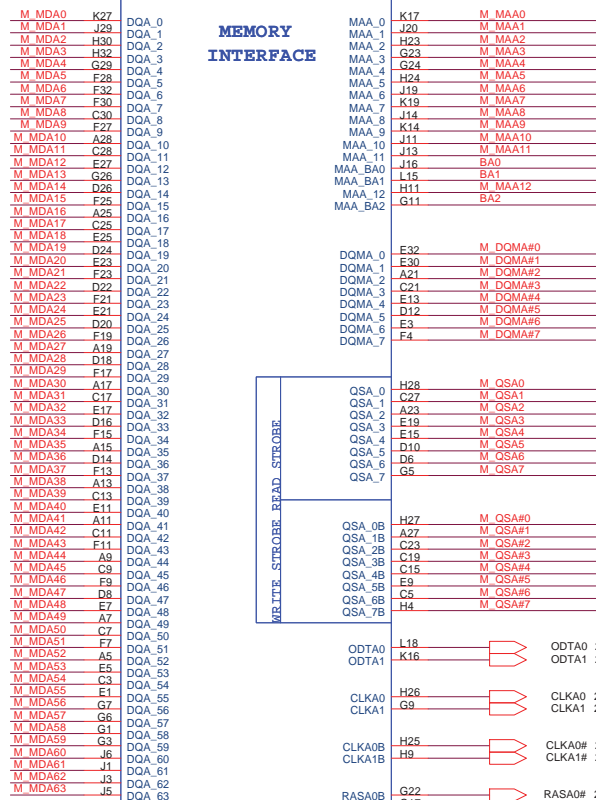




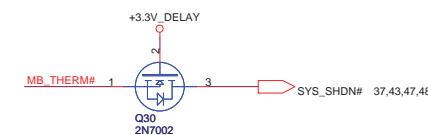
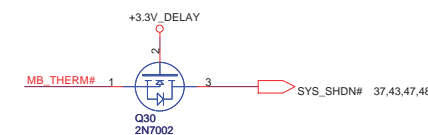
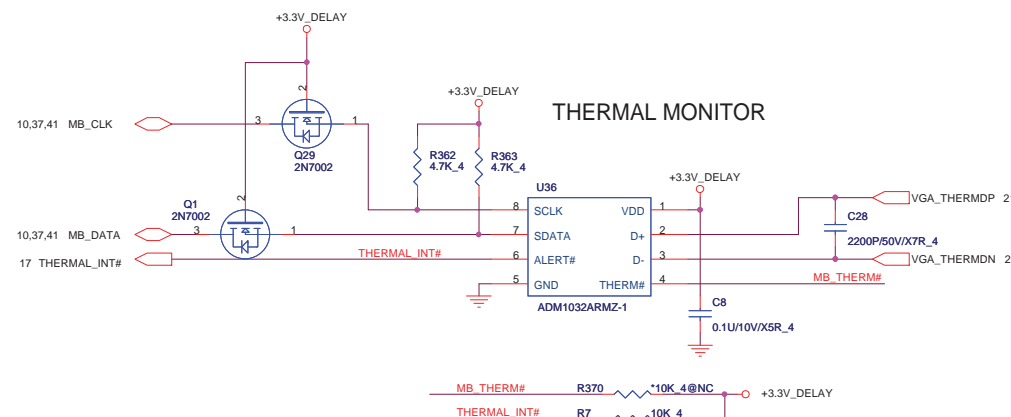
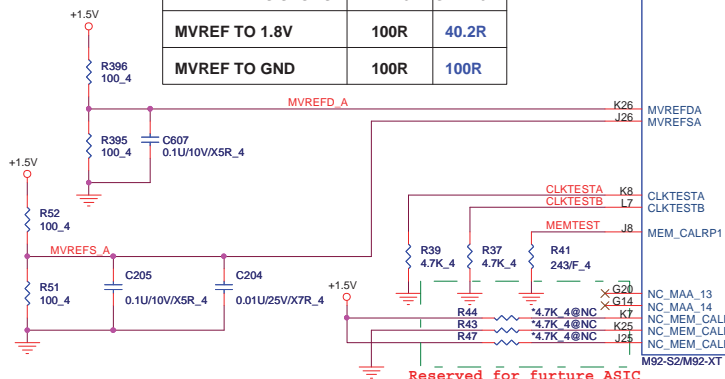
U40C

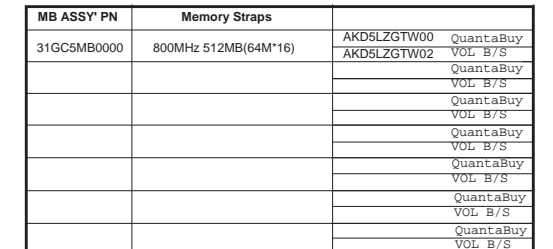
PART 3 OF 10

## MEMORY INTERFACE

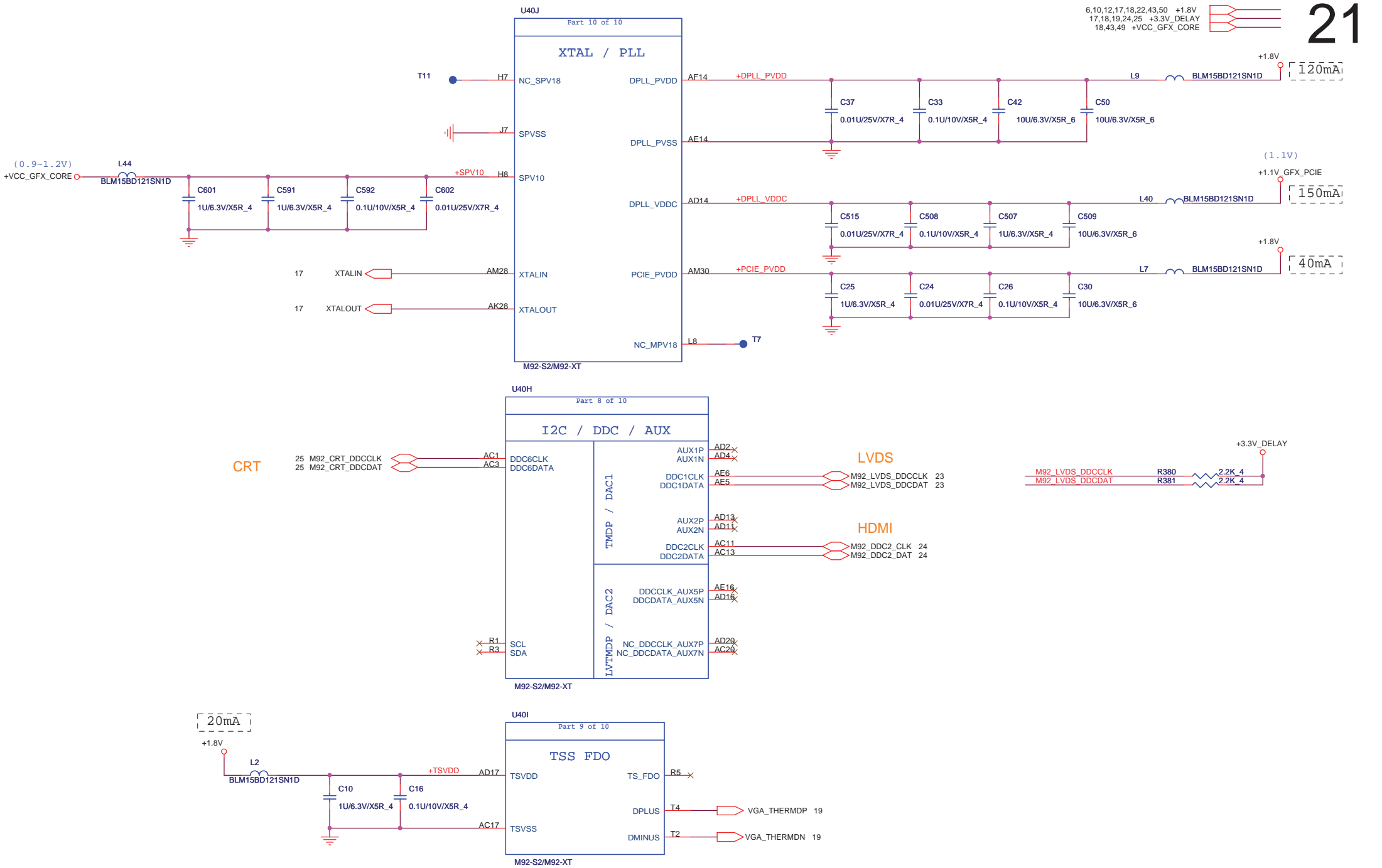


DIVIDER RESISTORS	DDR3	GDDR3
MVREF TO 1.8V	100R	40.2R
MVREF TO GND	100R	100R

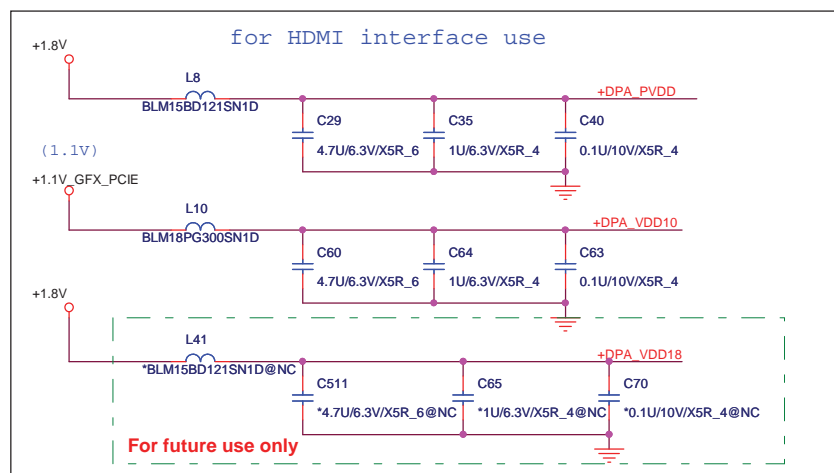
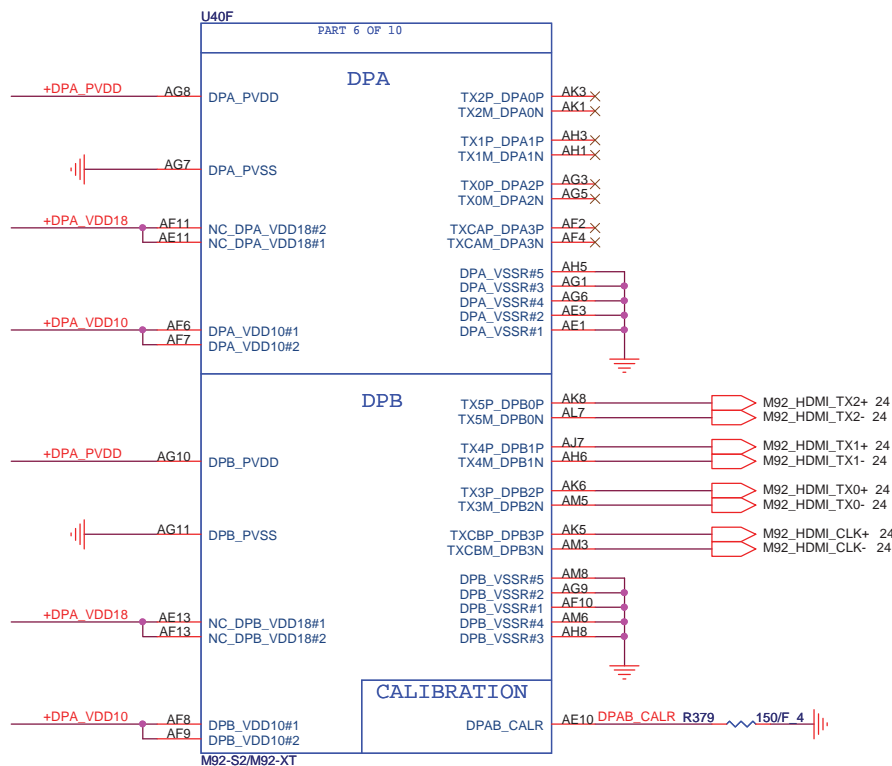




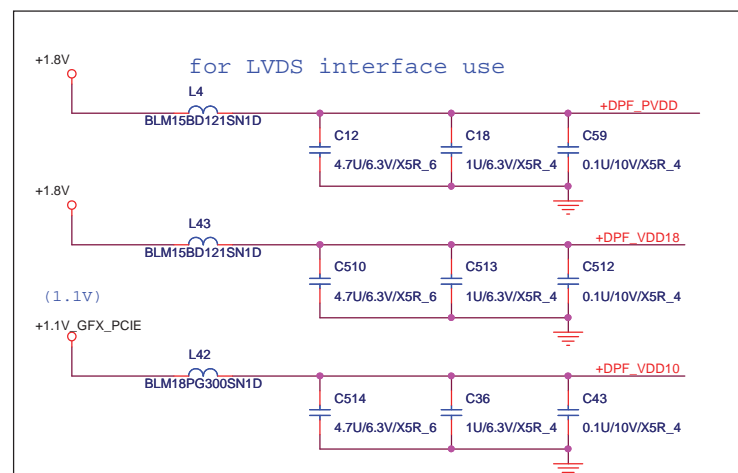
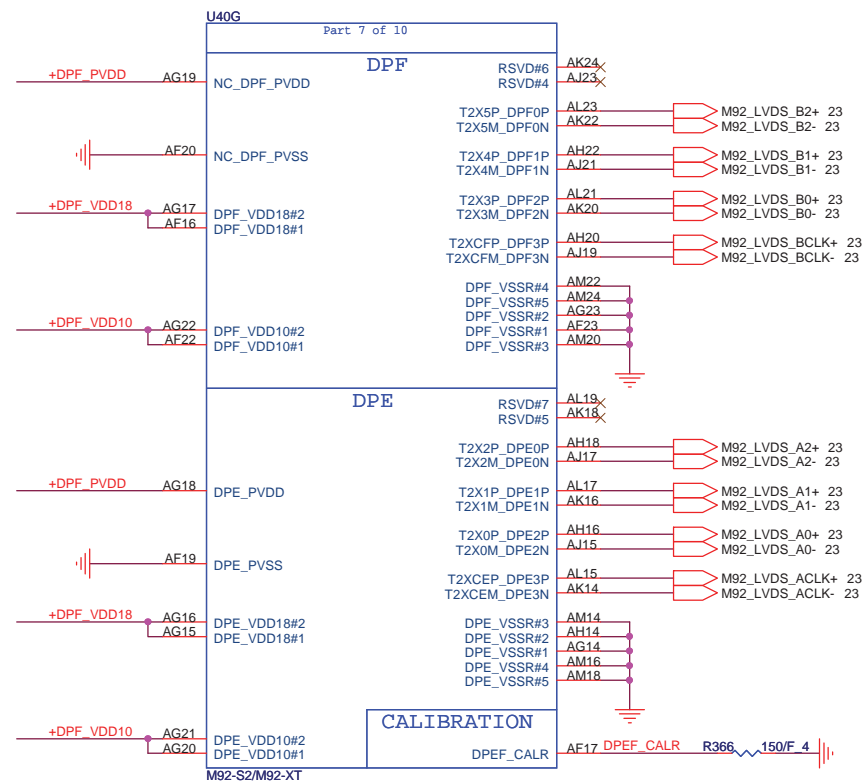




## TMDP(HDMI) INTERFACE



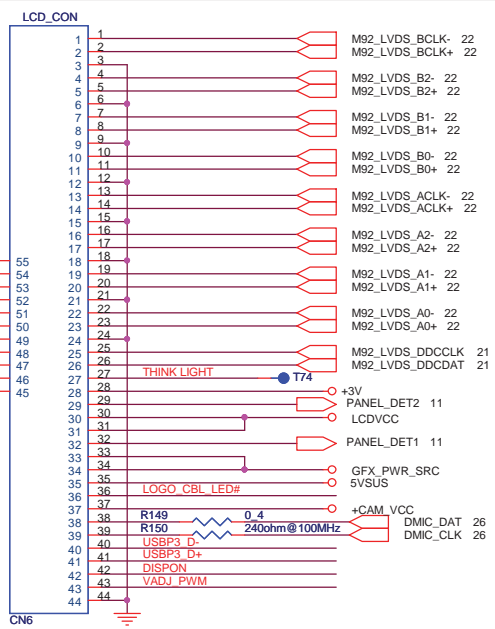
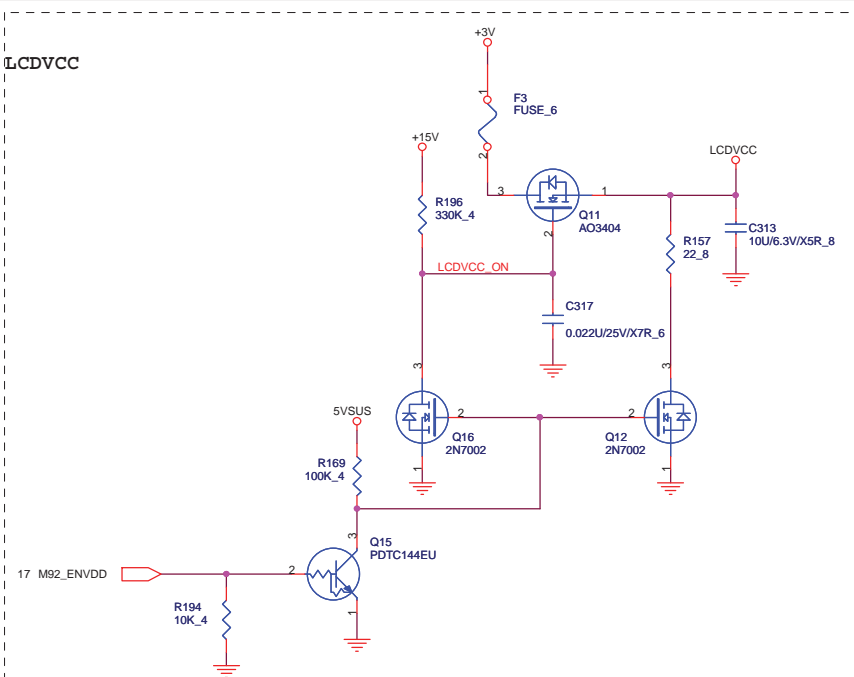
## LVDS INTERFACE



**LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

Size Custom Document Number **VGA-M92-XT (TMDP I/F) 7/7** Rev  
 Date: Saturday, November 07, 2009 Sheet 22 of 55

LCDVCC

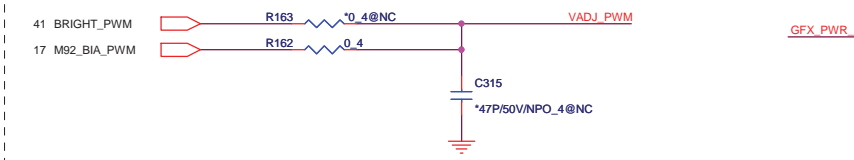
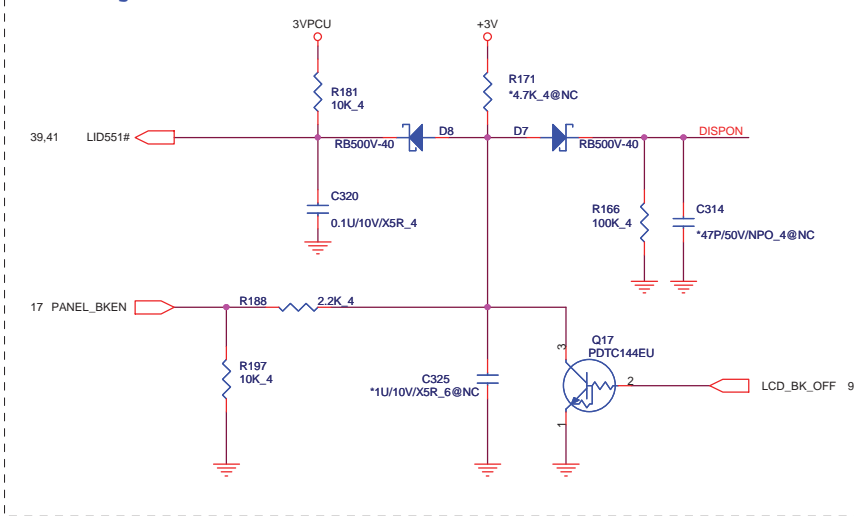


Address : A9H --Contrast  
AAH --Backlight

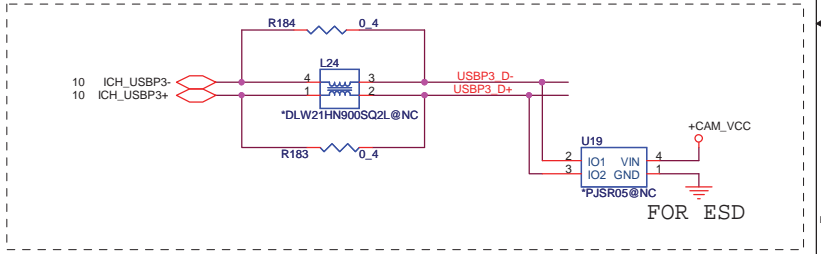
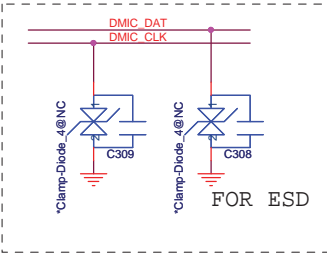
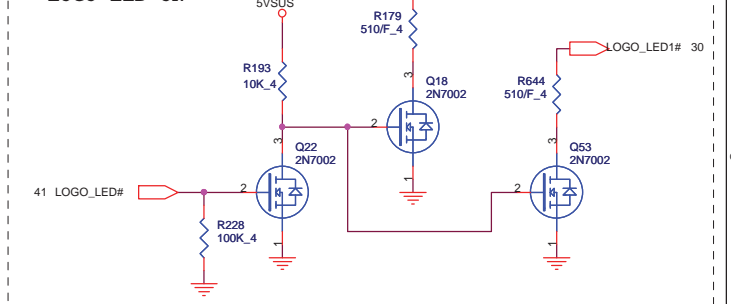
3,4,8,9,10,11,12,14,15,17,26,27,28,29,30,31,32,34,36,37,38,40,41,43,45,46,47,48,50,51  
38,43,45,47  
29,30,43,48  
9,27,35,39,41,43,44,47,49  
43,44,45,46,47,48,49,50  
12,18,24,25,26,28,35,37,39,41,43  
+3V  
+15V  
5VSUS  
3VPCU  
VIN  
+5V



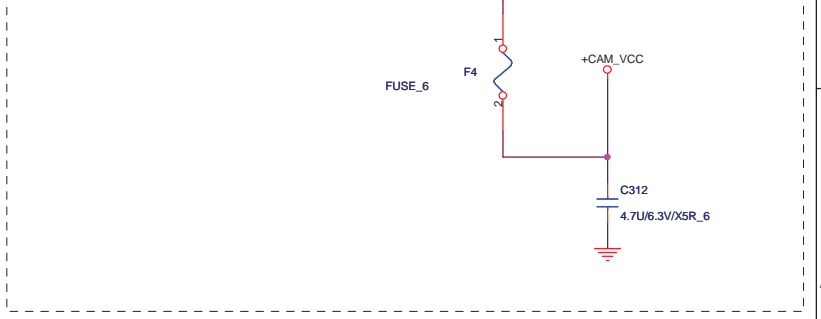
back light



LOGO LED ON



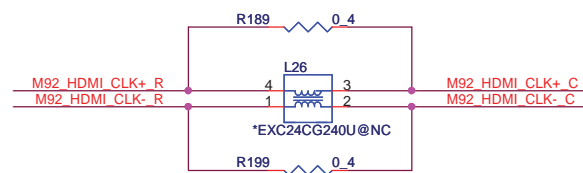
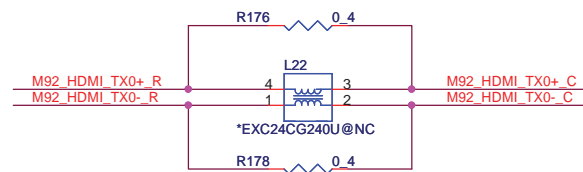
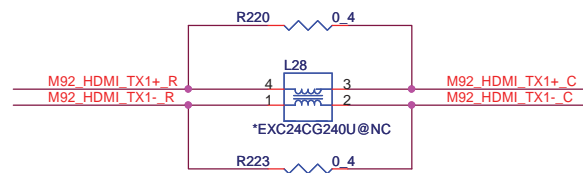
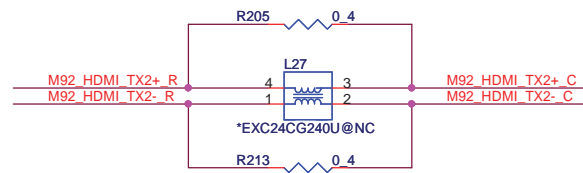
CAMERA VCC Control



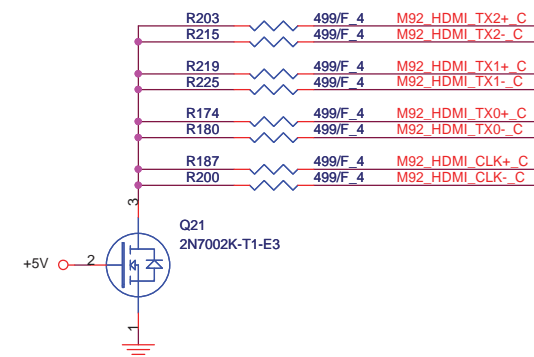
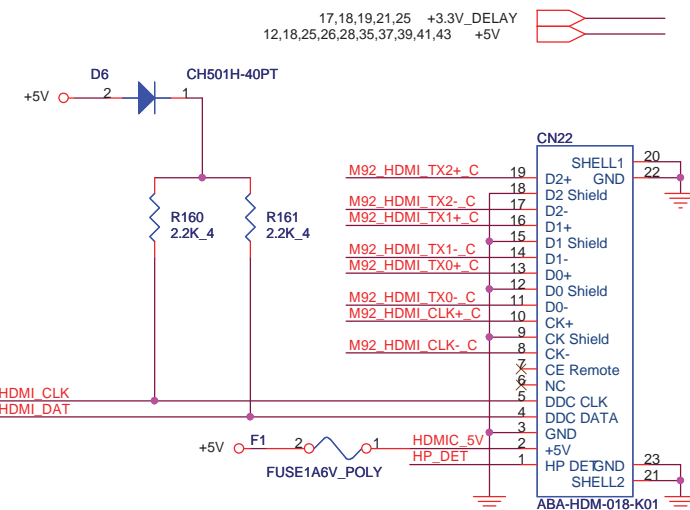
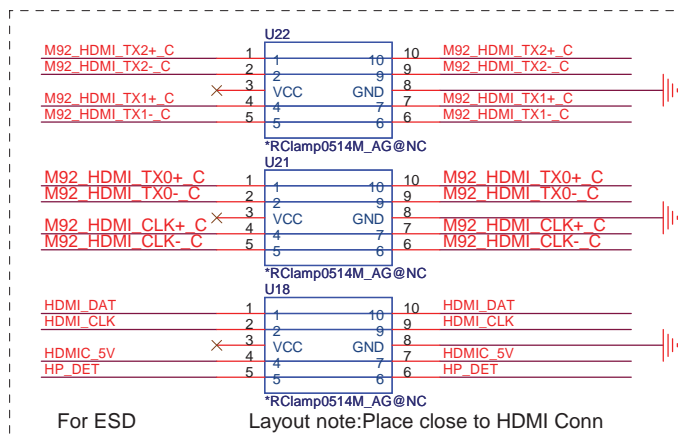
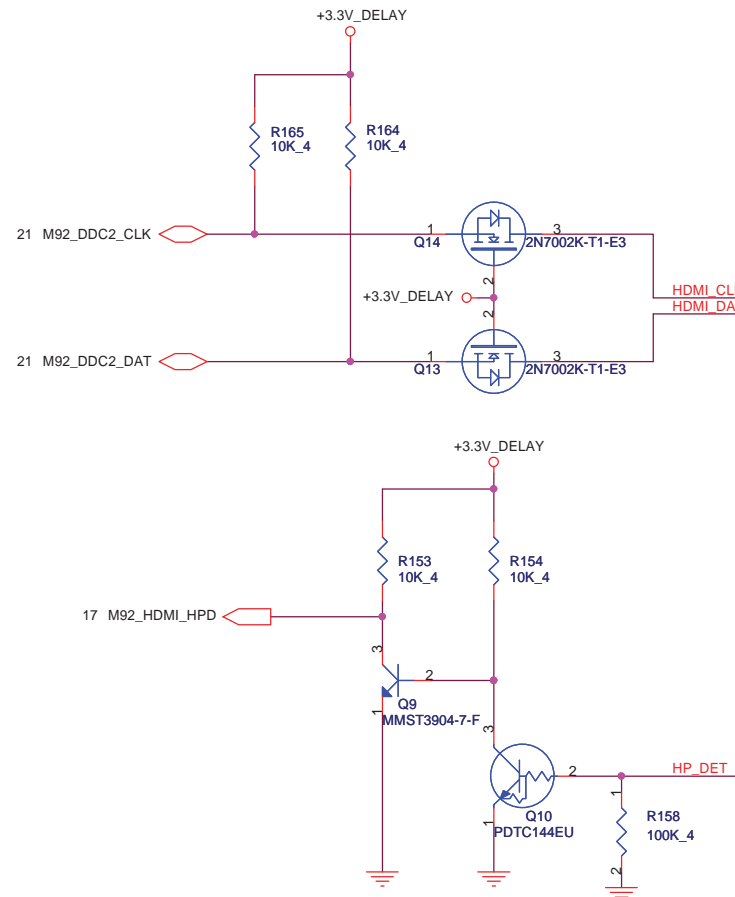
**LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

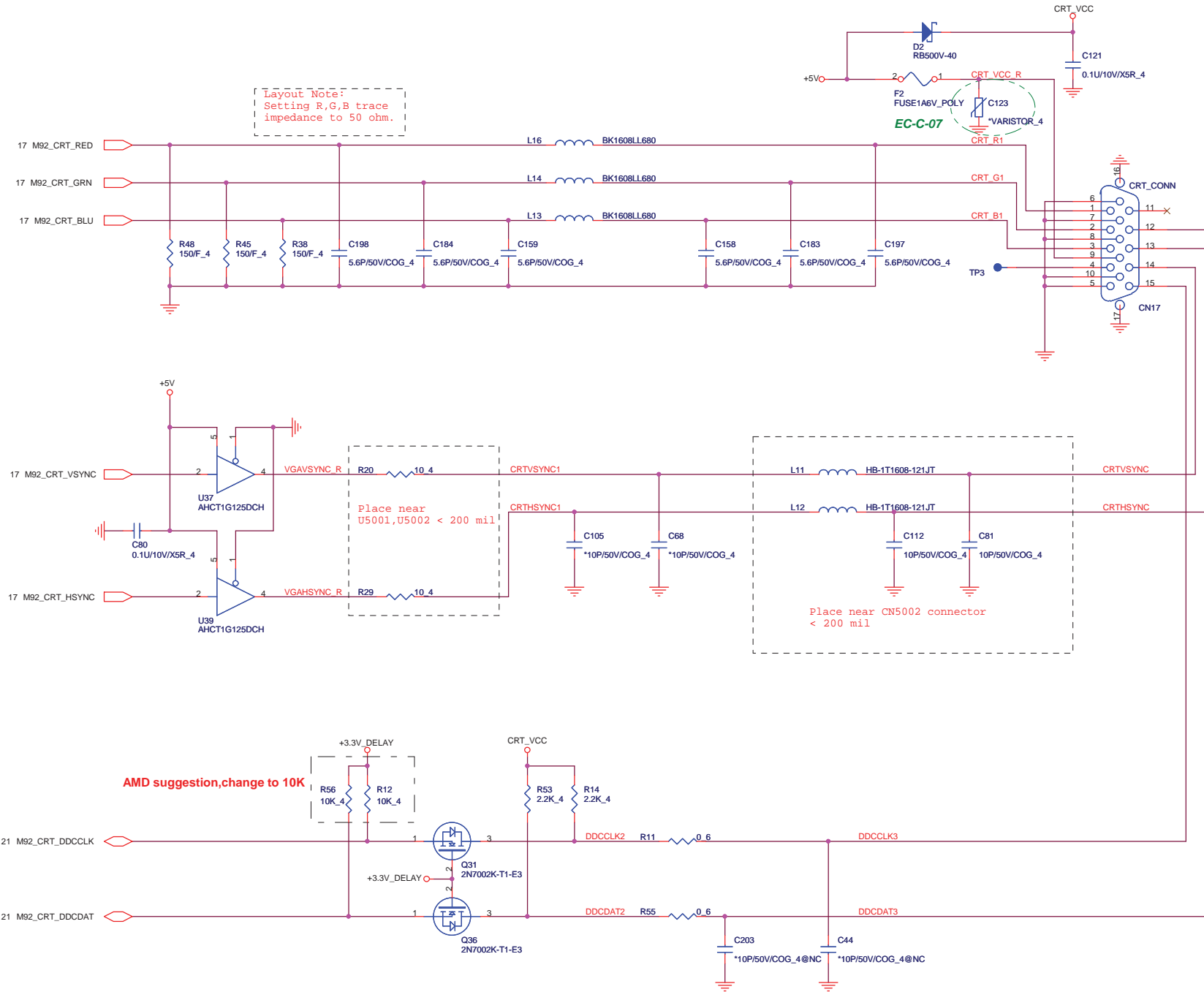
Size: Custom Document Number: LCD + CAMERA CONN Rev: 1

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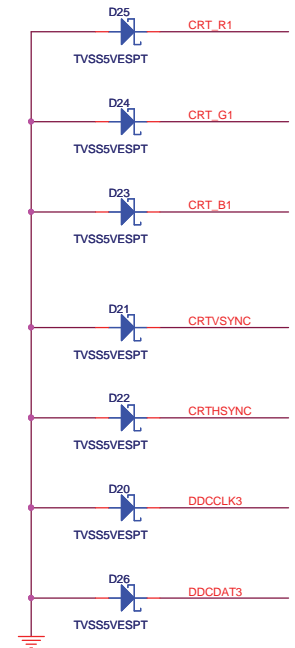


22 M92_HDMI_TX2+	C338	0.1U/10V/X5R_4	M92_HDMI_TX2+_R
22 M92_HDMI_TX2-	C348	0.1U/10V/X5R_4	M92_HDMI_TX2-_R
22 M92_HDMI_TX1+	C351	0.1U/10V/X5R_4	M92_HDMI_TX1+_R
22 M92_HDMI_TX1-	C354	0.1U/10V/X5R_4	M92_HDMI_TX1-_R
22 M92_HDMI_TX0+	C323	0.1U/10V/X5R_4	M92_HDMI_TX0+_R
22 M92_HDMI_TX0-	C327	0.1U/10V/X5R_4	M92_HDMI_TX0-_R
22 M92_HDMI_CLK+	C332	0.1U/10V/X5R_4	M92_HDMI_CLK+_R
22 M92_HDMI_CLK-	C335	0.1U/10V/X5R_4	M92_HDMI_CLK-_R



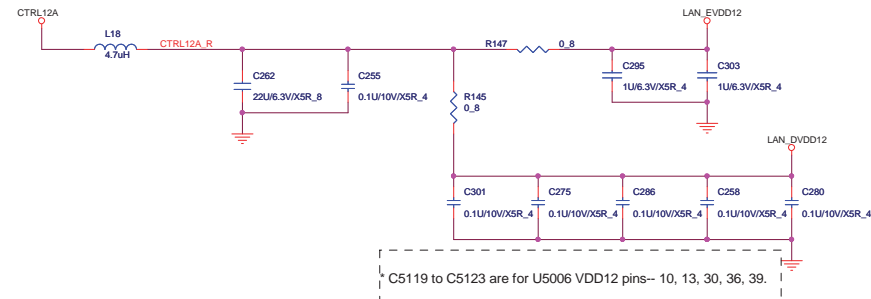
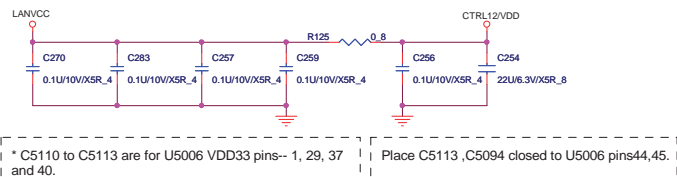
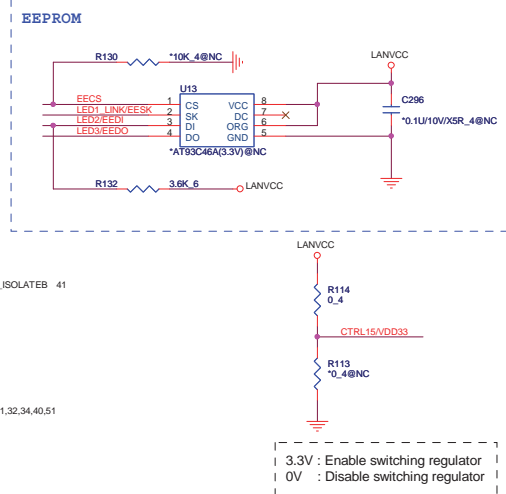
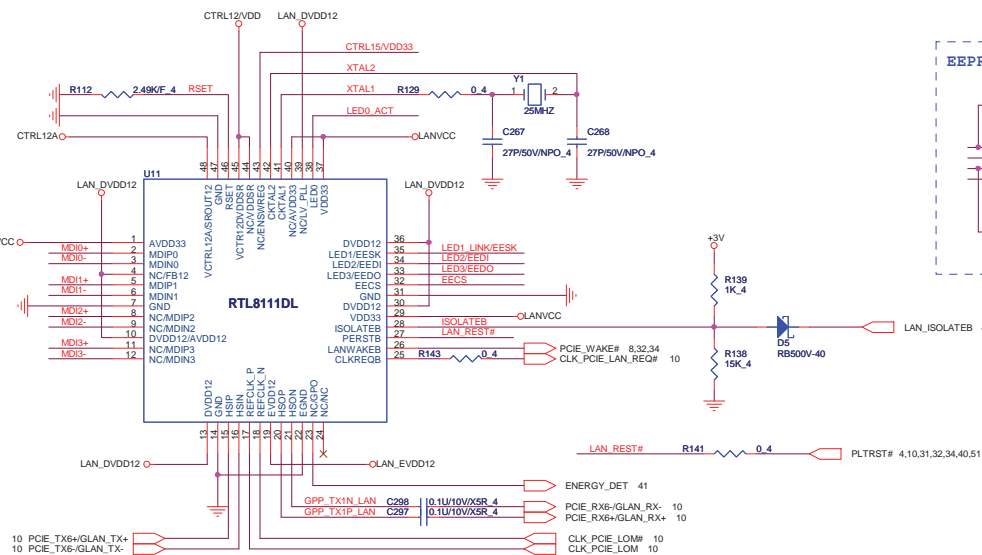
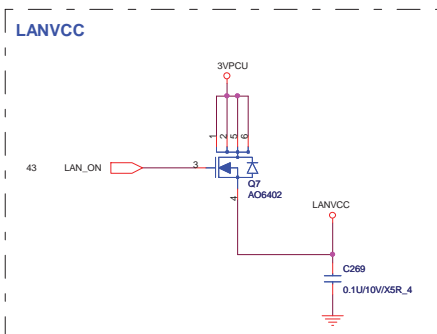


### ESD PROTECTION

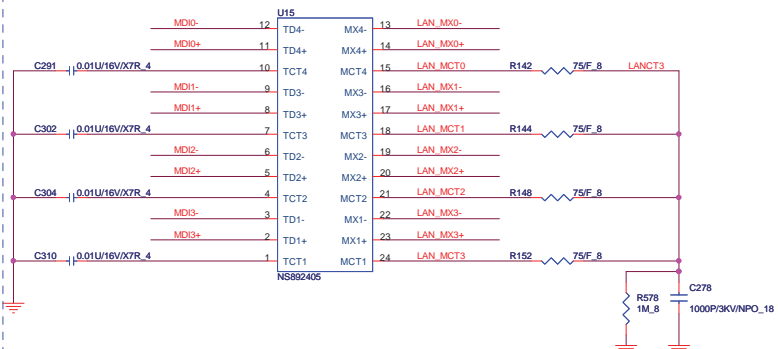




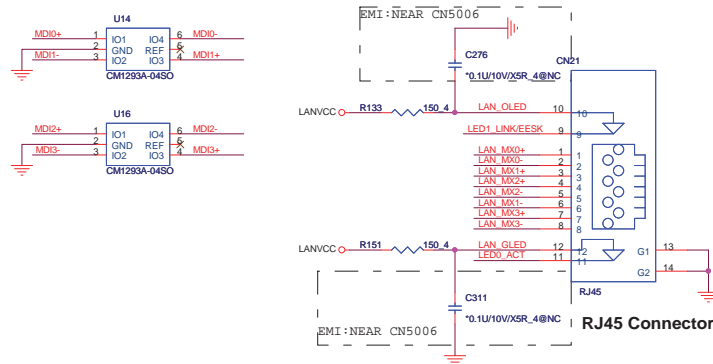




## Transformer



## RJ45 Connector

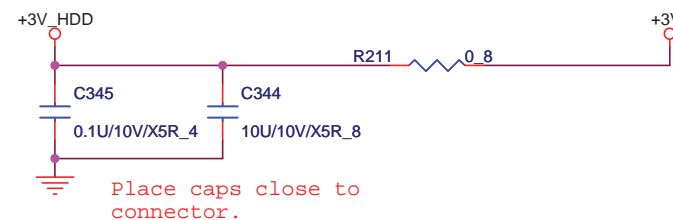
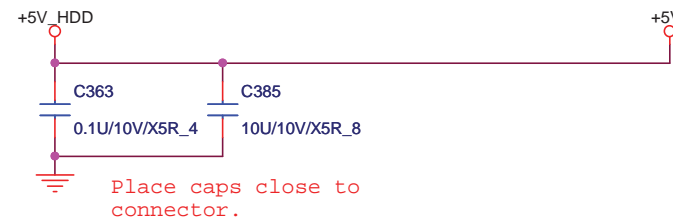
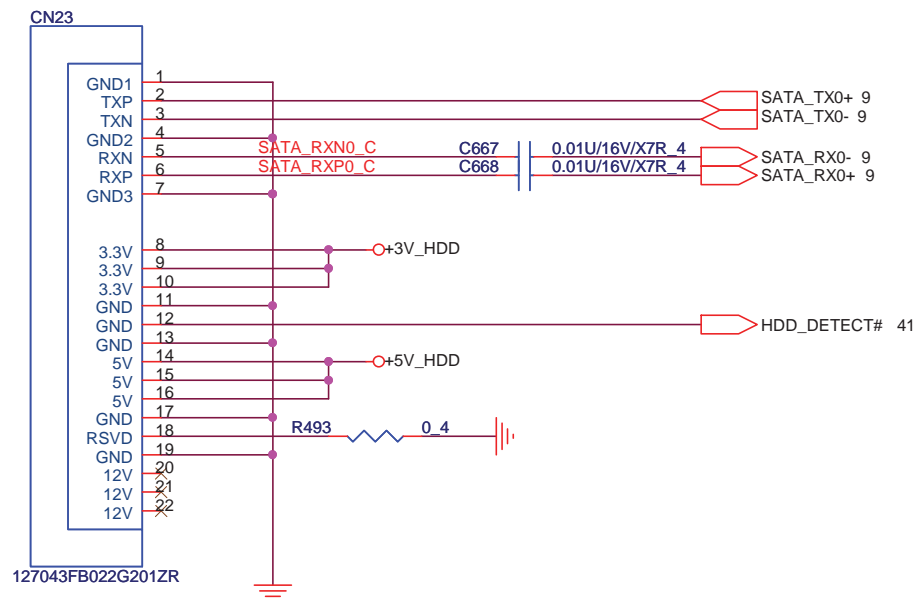


## SATA Connector.

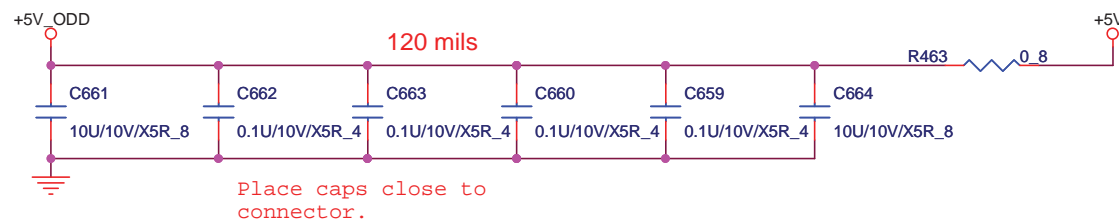
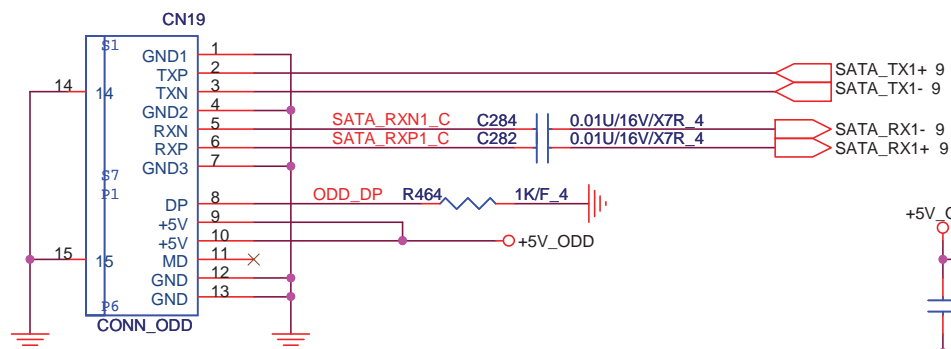
12,18,24,25,26,35,37,39,41,43 +5V  
3,4,8,9,10,11,12,14,15,17,23,26,27,29,30,31,32,34,36,37,38,40,41,43,45,46,47,48,50,51 +3V



# 28



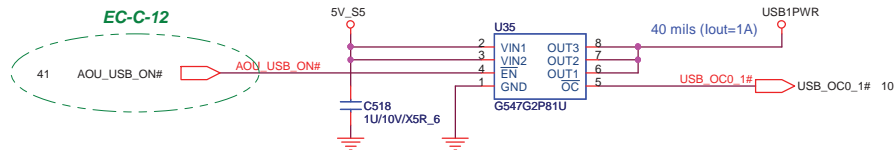
## ODD Connector



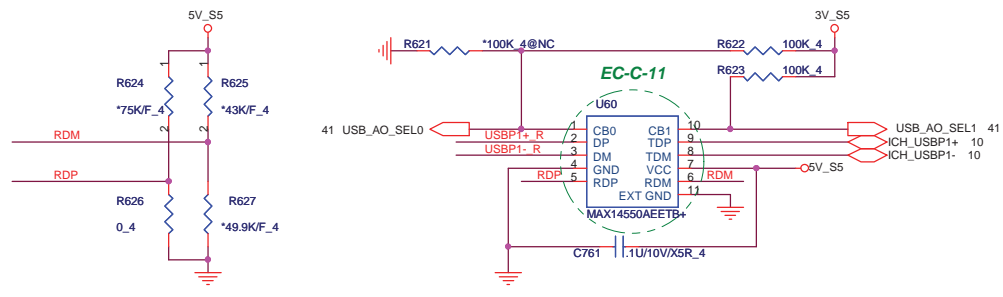
**LD-Note Calpella Discrete  
Quanta Computer Inc.**

Size Custom	Document Number <b>SATA (HDD&amp;CD_ROM)</b>	Rev
Date: Tuesday, November 03, 2009	Sheet 28 of 55	

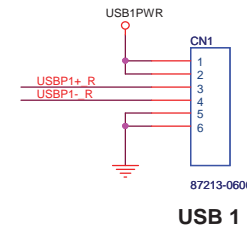
## USBX1



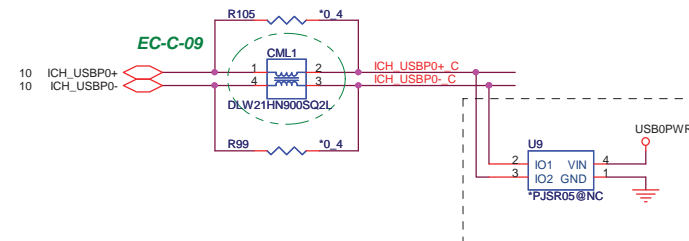
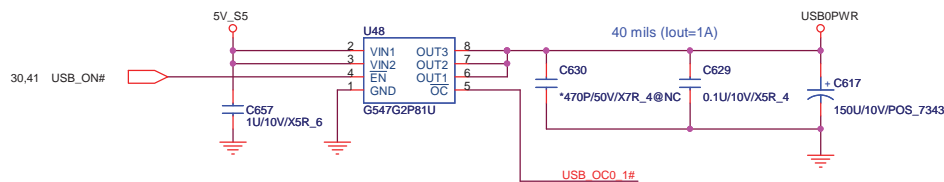
## Support Black-berry function



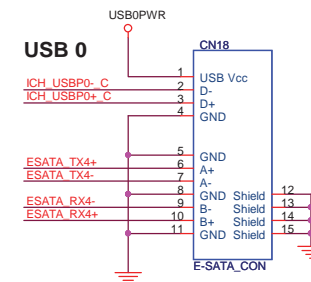
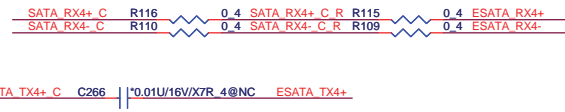
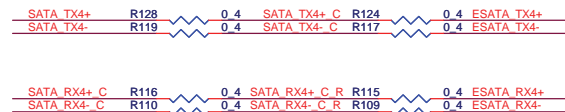
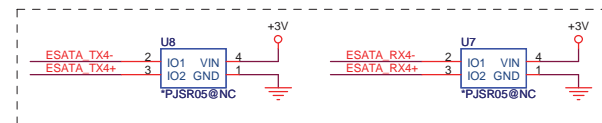
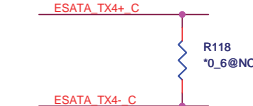
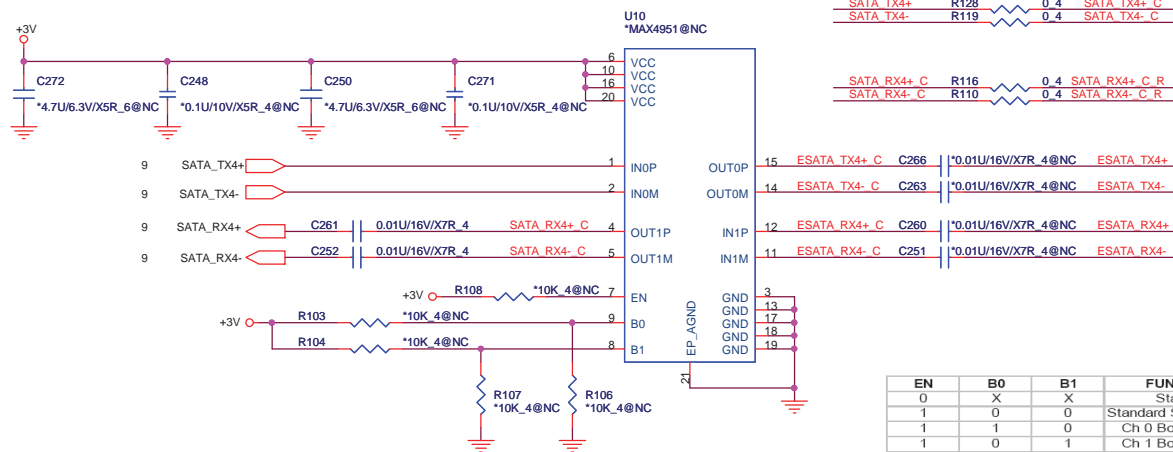
## USB X1----> Wire to board conn



## USB + E-SATA



## E-SATA RE-DRIVER



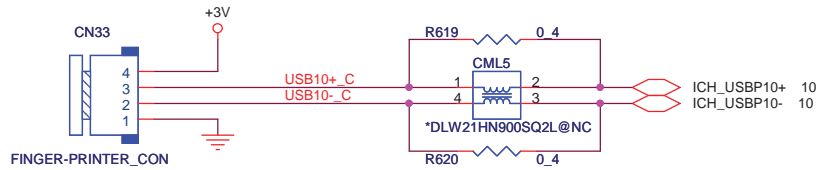
EN	B0	B1	FUNCTION
0	X	X	Standby
1	0	0	Standard SATA Output
1	1	0	Ch 0 Boost Output
1	0	1	Ch 1 Boost Output
1	1	1	Ch 0,1 Boost Output



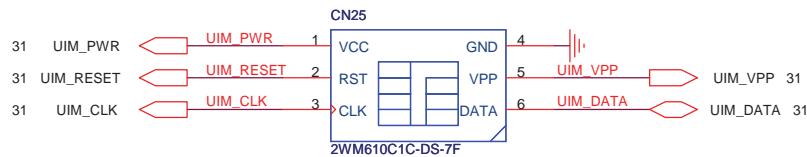
**LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

Size: Custom Document Number: **USB X1/USB+ESATA** Rev:   
Date: Sunday, November 08, 2009 Sheet: 29 of 55

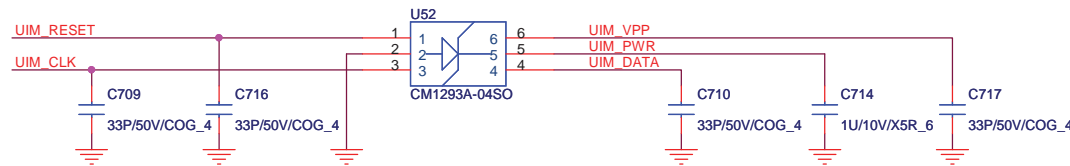
## Finger Print



## SIM Card CONN



**Layout Note:**  
UIM\_RESET, UIM\_CLK, UIM\_DATA routing as short as possible



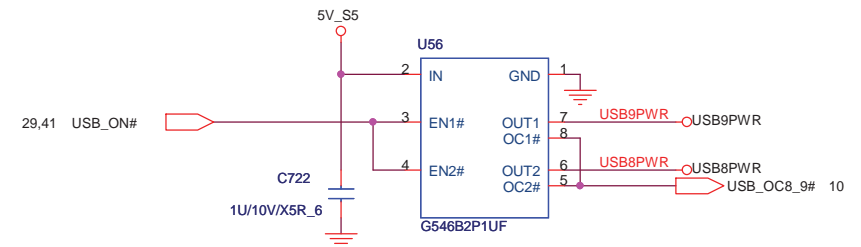
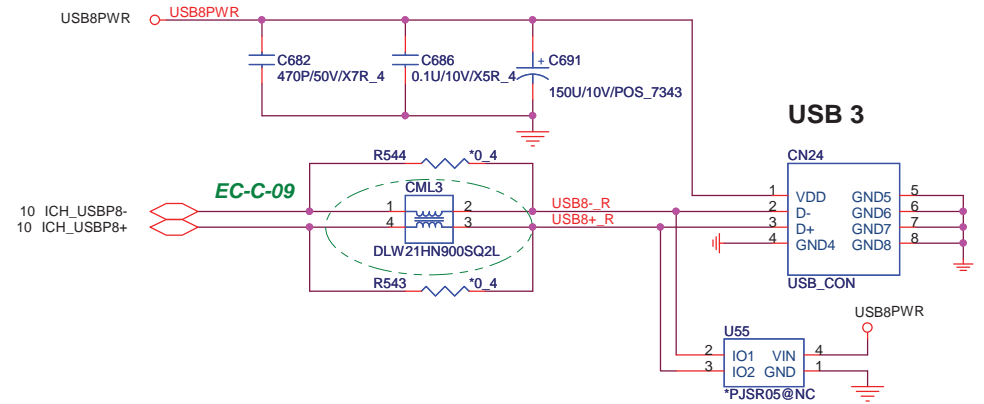
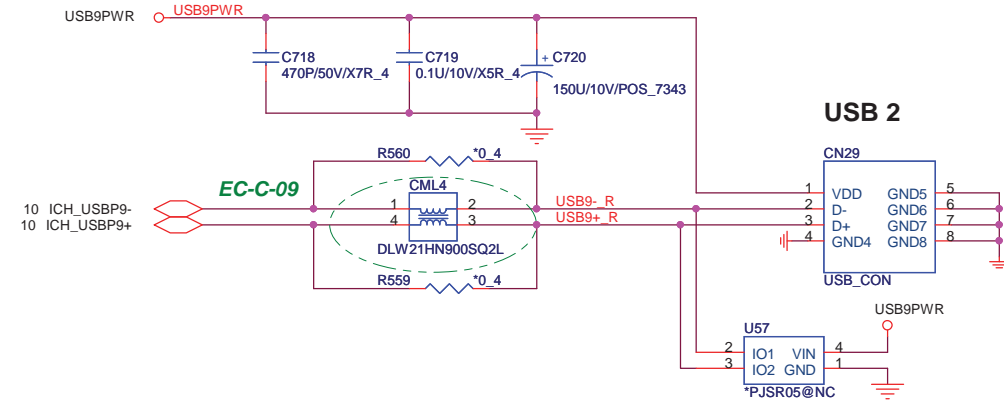
## LOGO LED



3,4,8,9,10,11,12,14,15,17,23,26,27,28,29,31,32,34,36,37,38,40,41,43,45,46,47,48,50,51  
9,23,27,35,39,41,43,44,47,49  
33,34,41,43,49  
23,29,43,48

+3V  
3VPCU  
3VSUS  
5VSUS

30

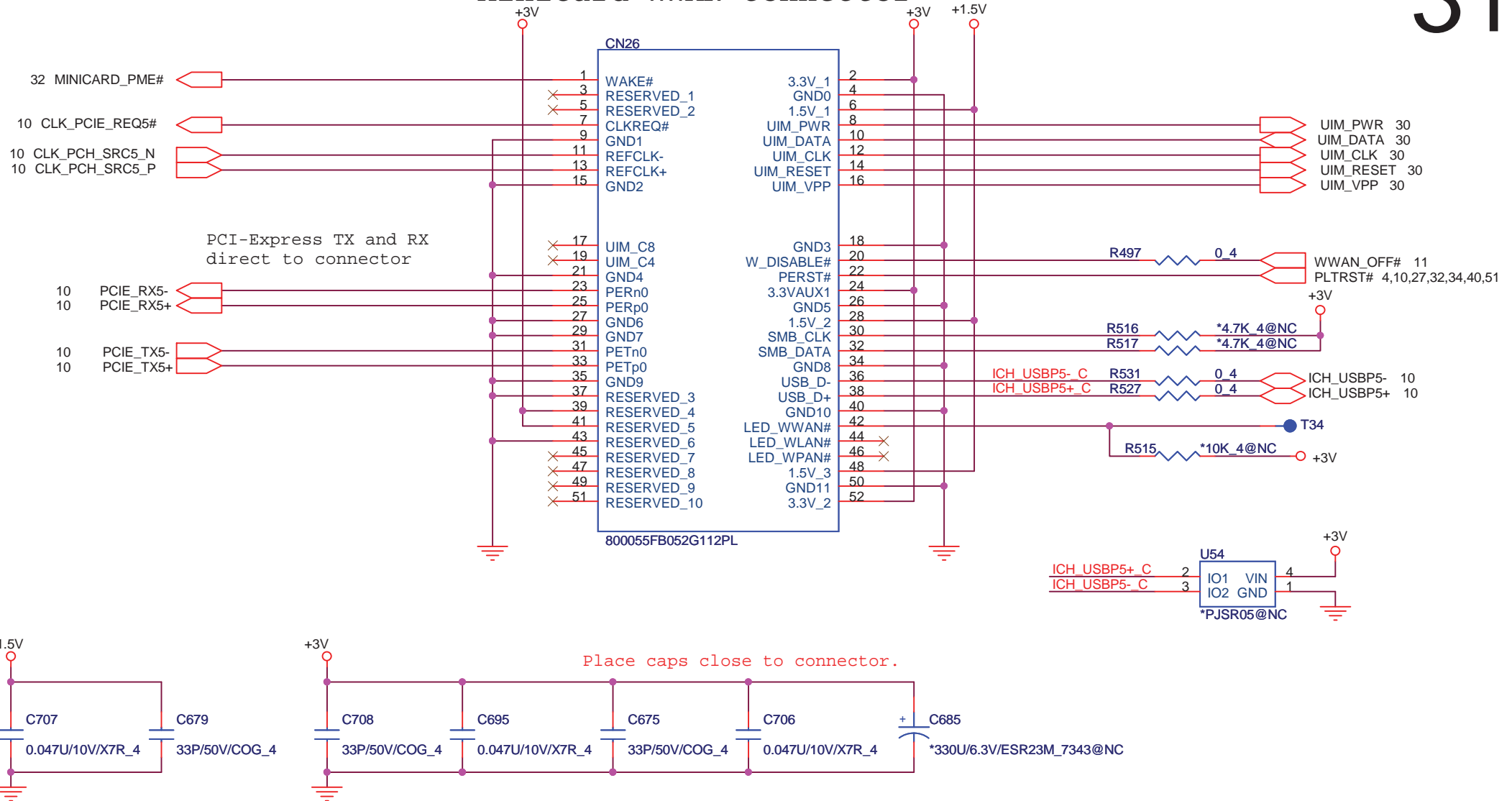


**LD-Note Calpella Discrete  
Quanta Computer Inc.**

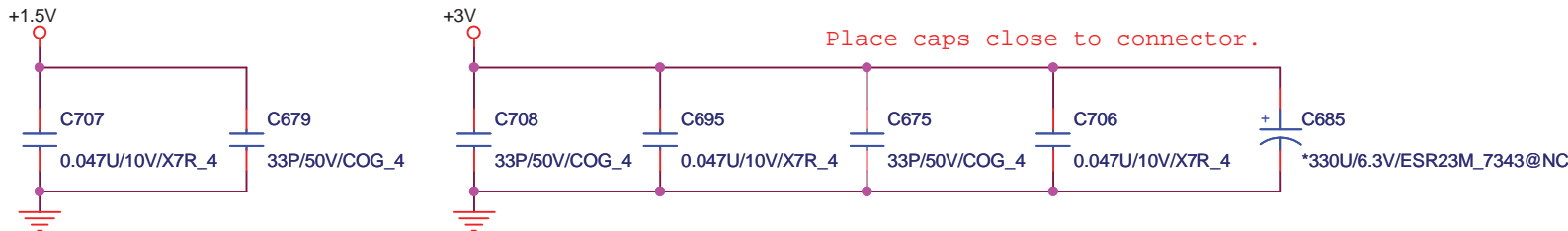
Size	Document Number	Rev
Custom	USB X2/SIM_CARD/LEDs/RF	
Date:	Sunday, November 08, 2009	Sheet 30 of 55

# MiniCard WWAN connector

31



Place caps close to connector.

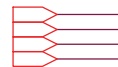


LD-Note Calpella Discrete  
Quanta Computer Inc.

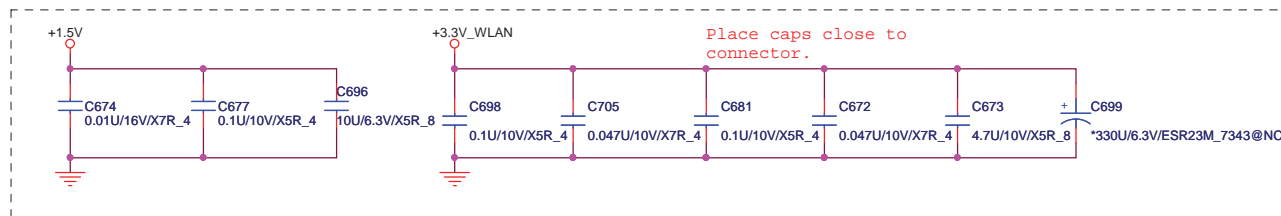
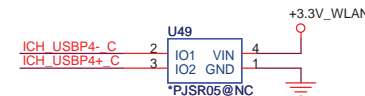
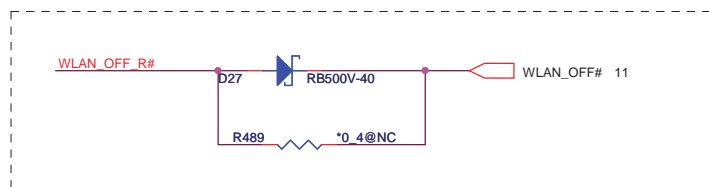
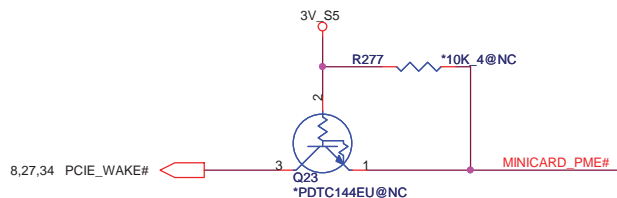
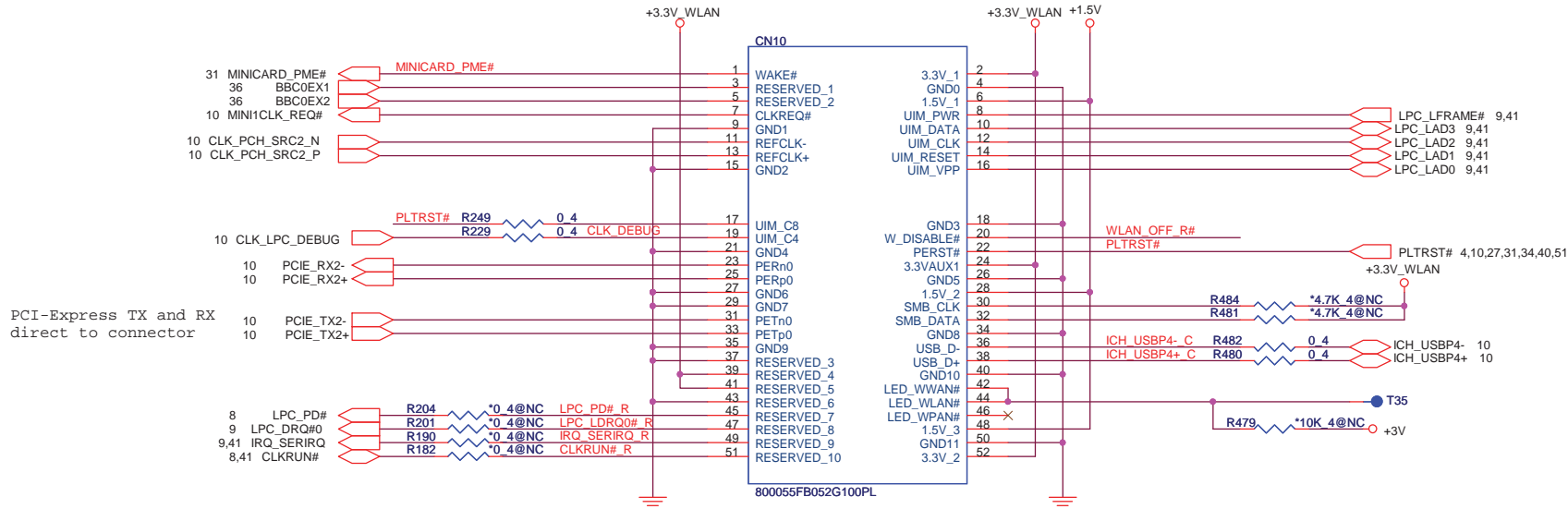
Size Custom	Document Number <b>MINI-Card (WWAN)</b>	Rev
Date: Tuesday, November 03, 2009	Sheet 31 of 55	

# MiniCard WLAN/WiMAX connector

3,4,8,9,10,11,12,14,15,17,23,26,27,28,29,30,31,34,36,37,38,40,41,43,45,46,47,48,50,51  
 3,18,19,20,31,34,45  
 9,23,27,35,39,41,43,44,47,49  
 23,43,44,45,46,47,48,49,50  
 +3V  
 +1.5V  
 3VPCU  
 VIN



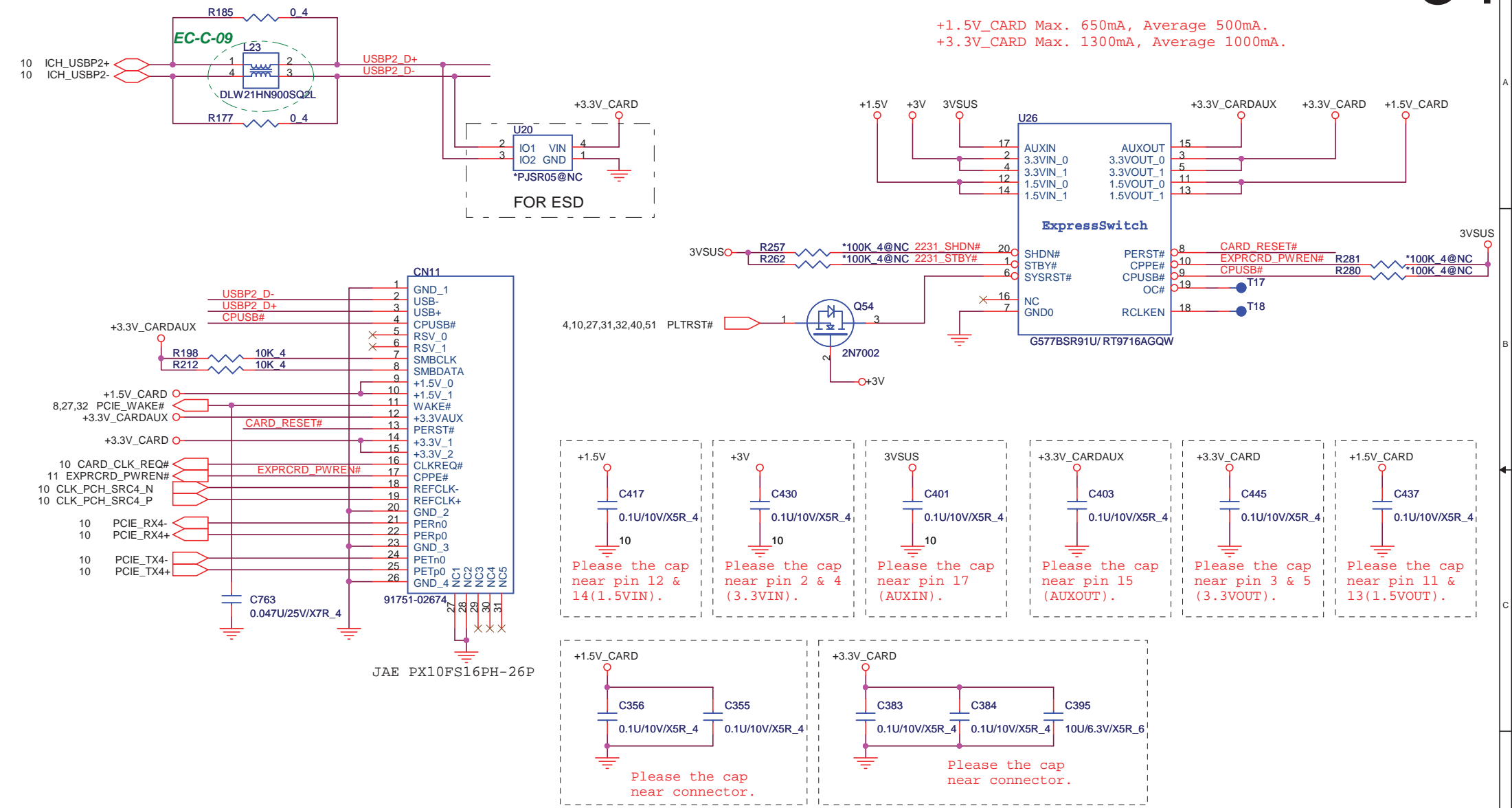
32







Express Card



+1.5V\_CARD Max. 650mA, Average 500mA.  
+3.3V\_CARD Max. 1300mA, Average 1000mA.

Please the cap near pin 12 & 14 (1.5VIN).  
Please the cap near pin 2 & 4 (3.3VIN).  
Please the cap near pin 17 (AUXIN).  
Please the cap near pin 15 (AUXOUT).  
Please the cap near pin 3 & 5 (3.3VOUT).  
Please the cap near pin 11 & 13 (1.5VOUT).  
Please the cap near connector.  
Please the cap near connector.

Size Custom

Document Number

Express Card

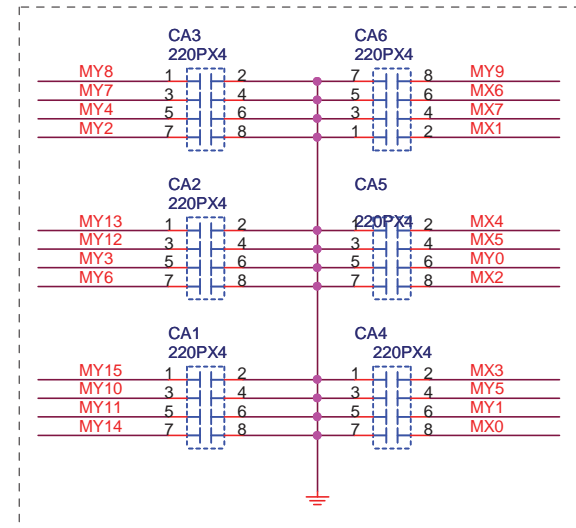
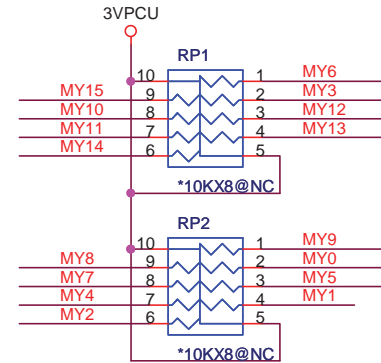
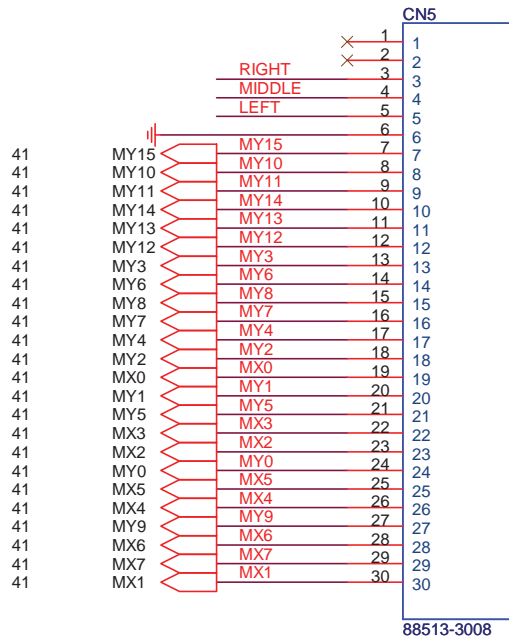
Rev

Date: Saturday, November 07, 2009

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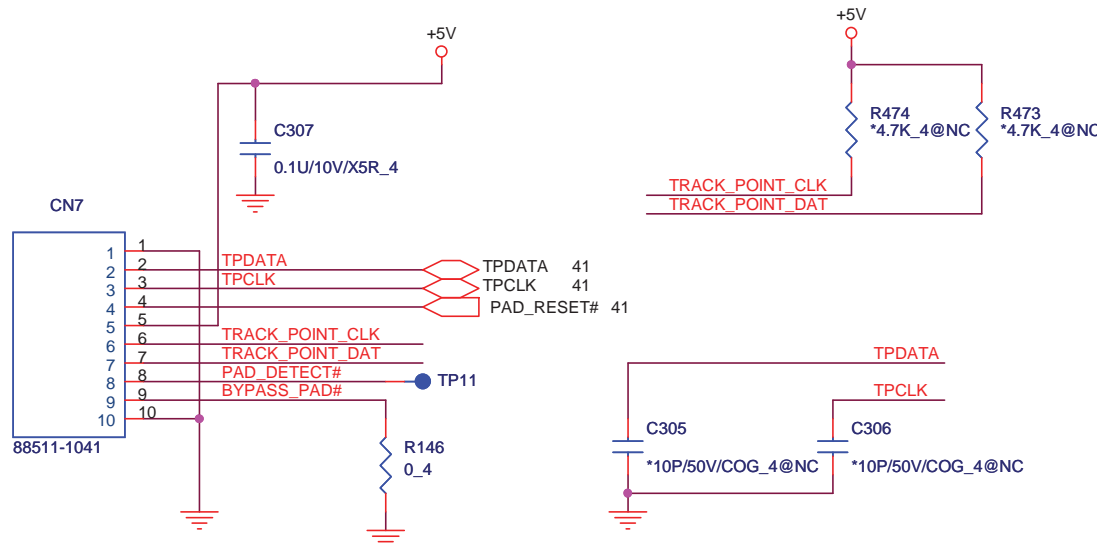
# KEYBOARD

## KEYBOARD connector



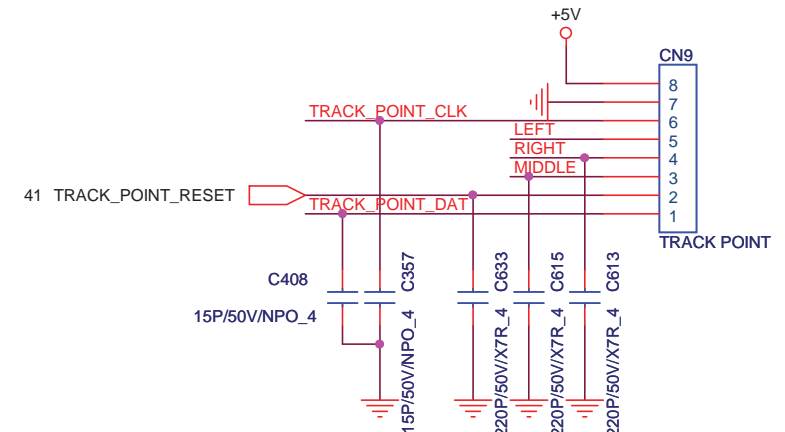
For EMI request

# Touch pad



Place C5303 ,C5302 closed to CN5020

# TRACK POINT



**LD-Note Calpella Discrete  
Quanta Computer Inc.**

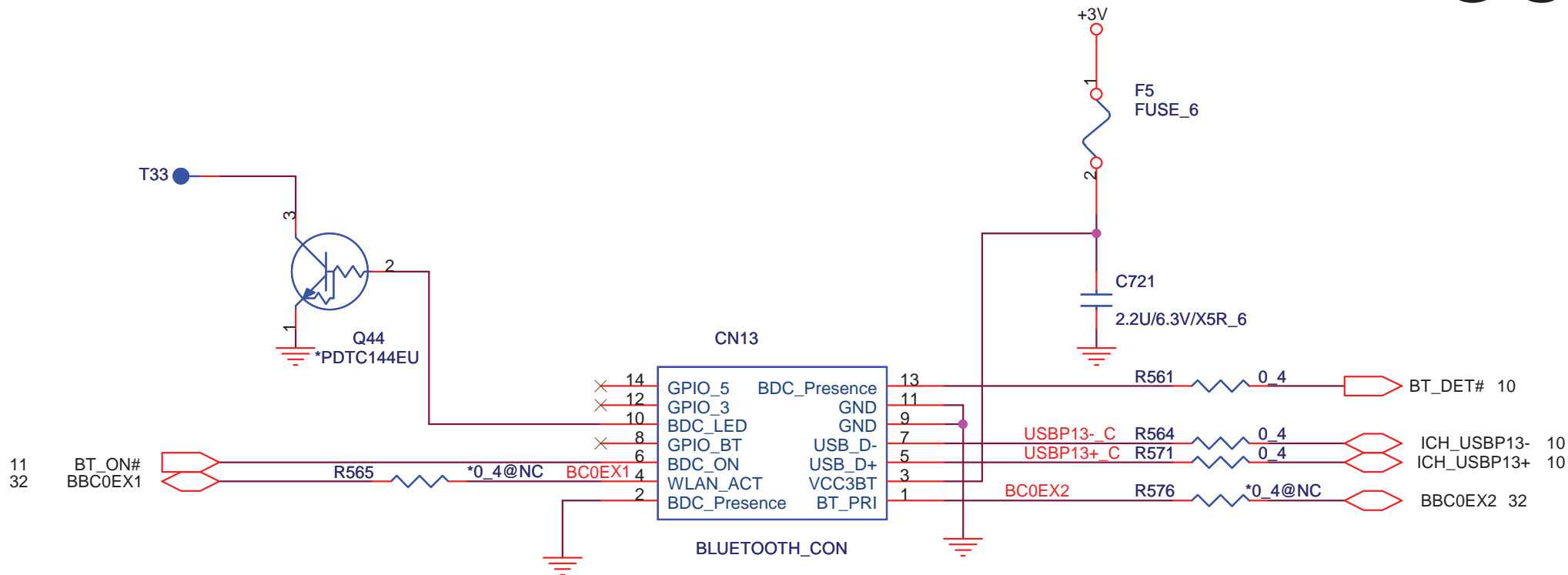
Size	Document Number	Rev
Custom	K/B, T/P	
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# BLUETOOTH

3,4,8,9,10,11,12,14,15,17,23,26,27,28,29,30,31,32,34,37,38,40,41,43,45,46,47,48,50,51

+3V

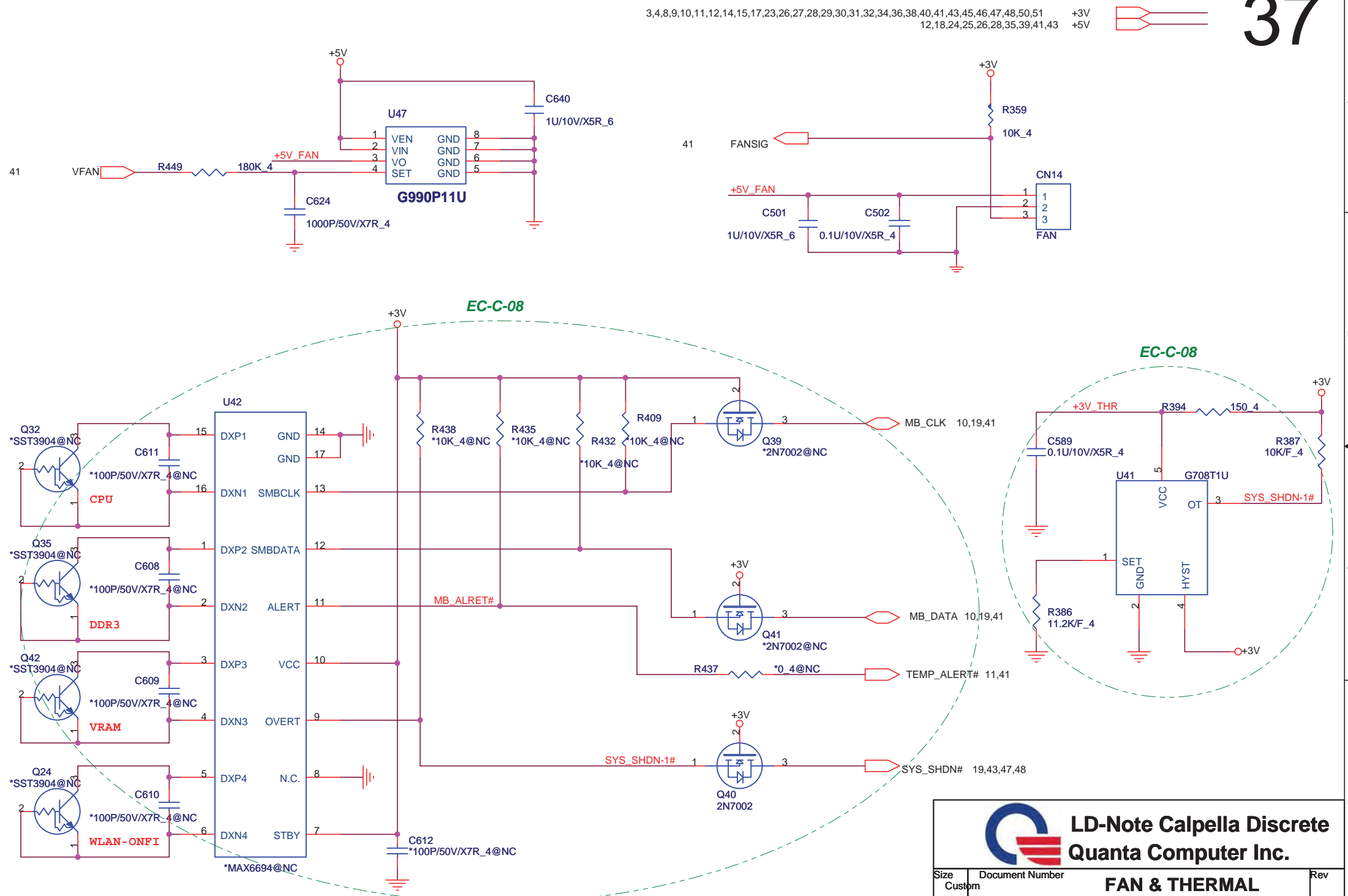
36




**LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

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FAN CONTROL





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Quanta Computer Inc.**

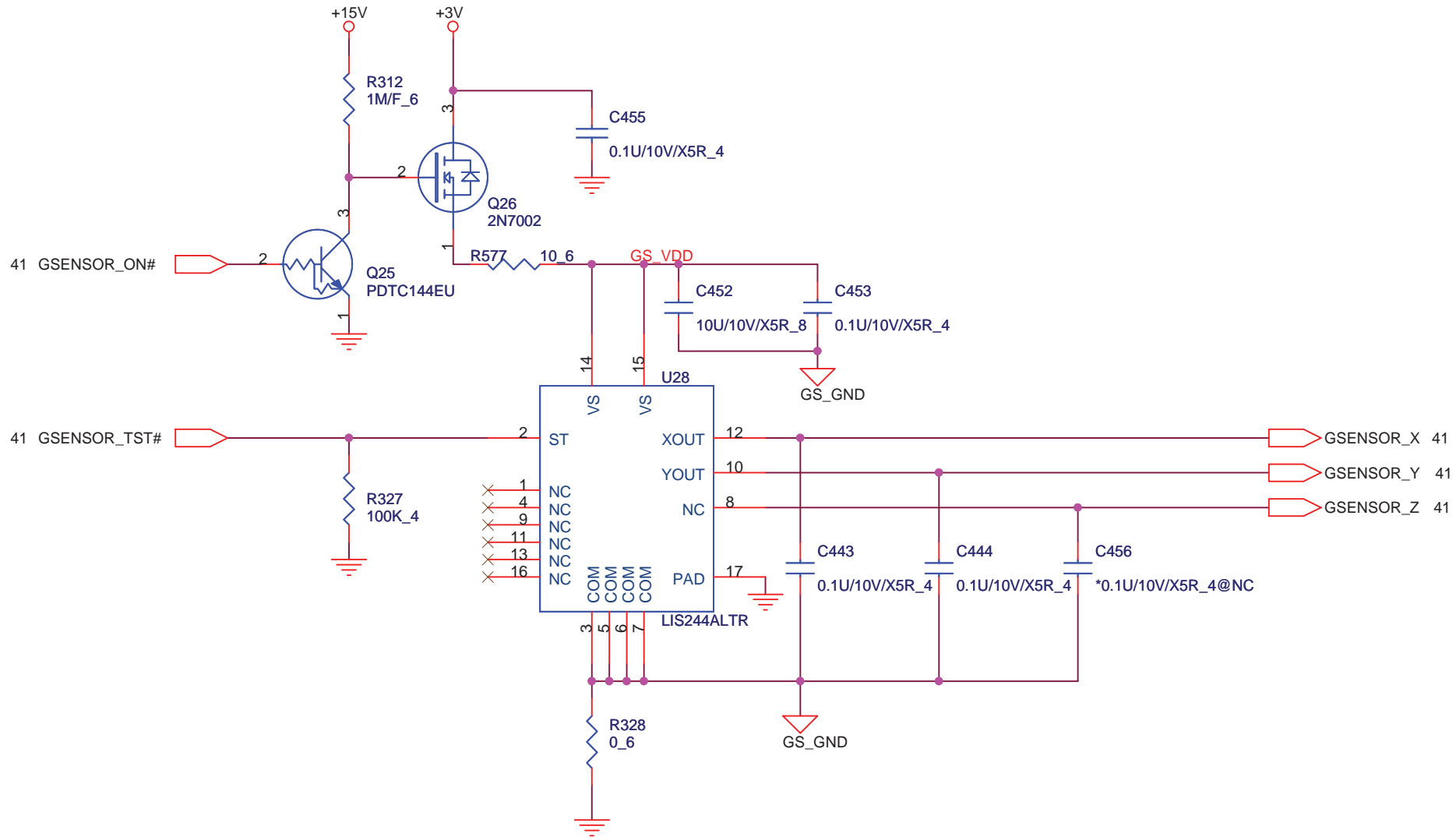
Size Custom	Document Number	<b>FAN &amp; THERMAL</b>	Rev
Date:	Saturday, November 07, 2009	Sheet 37 of 55	

# G-SENSOR (2-Axial)

3,4,8,9,10,11,12,14,15,17,23,26,27,28,29,30,31,32,34,36,37,40,41,43,45,46,47,48,50,51  
+3V  
23,43,45,47 +15V



38



**LD-Note Calpella Discrete  
Quanta Computer Inc.**

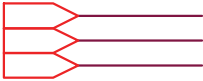
Size: Custom Document Number: **G-SENSOR** Rev:

Date: Tuesday, November 03, 2009 Sheet 38 of 55

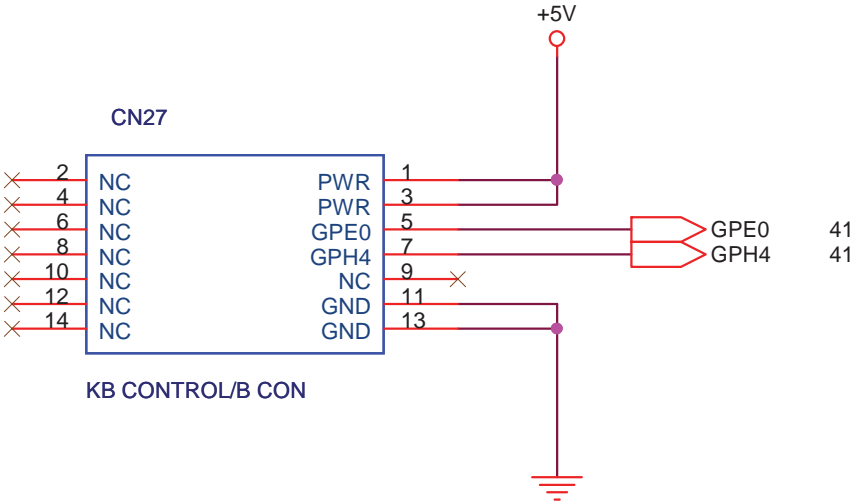
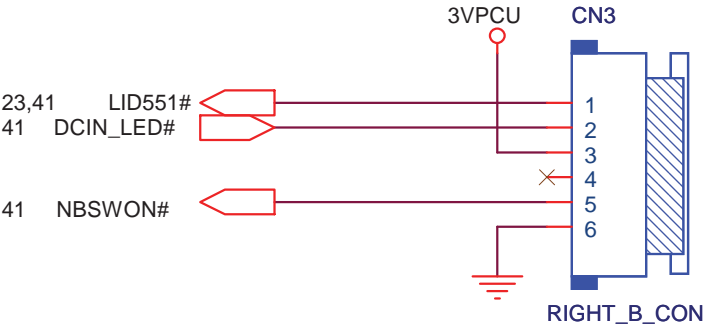


3,4,8,9,10,11,12,14,15,17,23,26,27,28,29,30,31,32,34,36,37,38,40,41,43,45,46,47,48,50,51  
9,23,27,35,41,43,44,47,49  
12,18,24,25,26,28,35,37,41,43

+3V  
3VPCU  
+5V



FFC TO B LED RIGHT SIDE CONNECTOR(For 14",15")



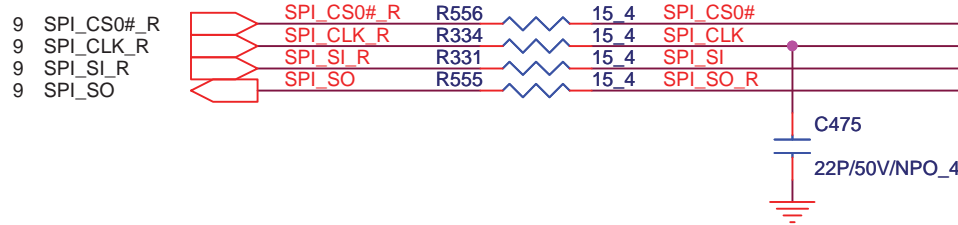
LD-Note Calpella Discrete  
Quanta Computer Inc.

## iTPM ENABLE/DISABLE

EC-C-04



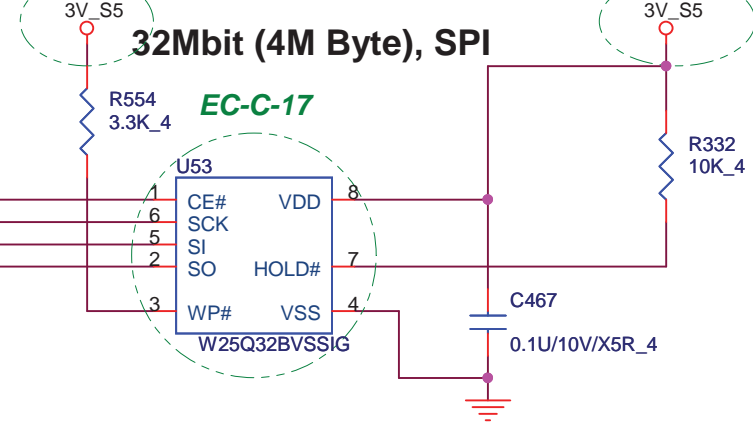
iTPM Function	R292
Enable	1K
Disable	NC



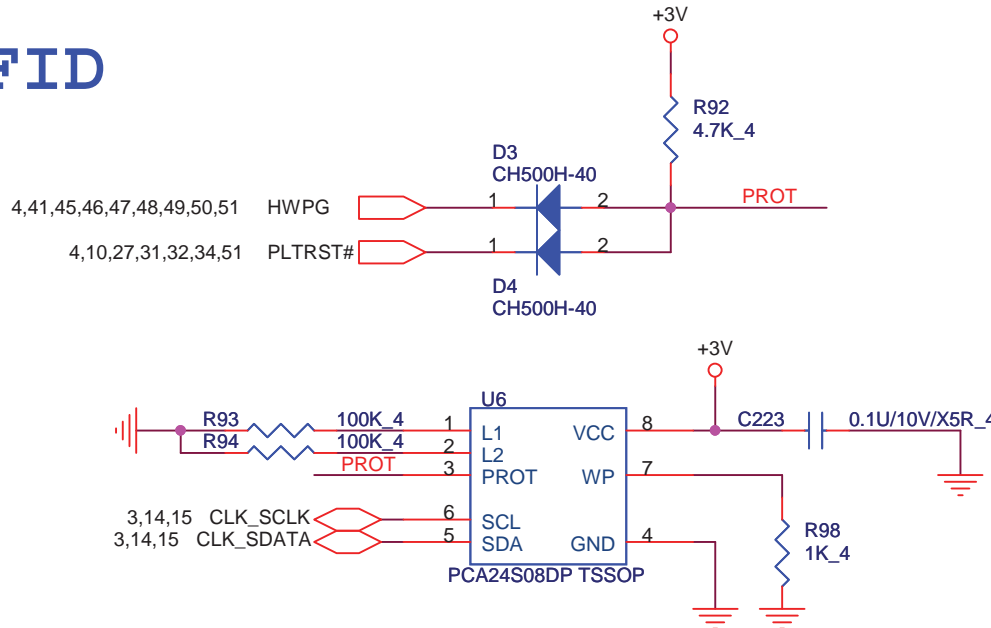
EC-C-04

For PCH

32Mbit (4M Byte), SPI

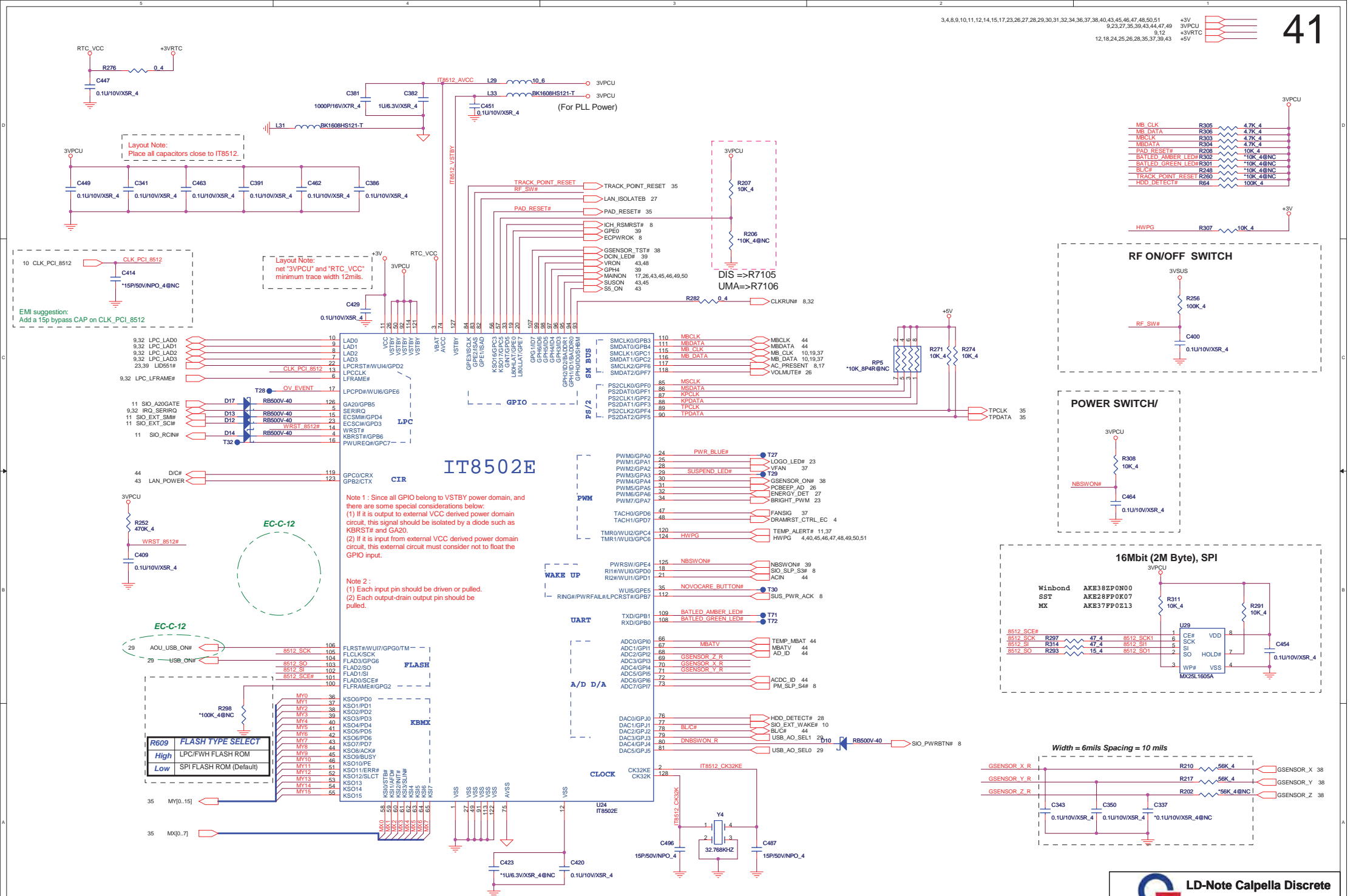


## RFID

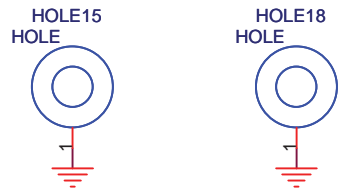


**LD-Note Calpella Discrete  
Quanta Computer Inc.**

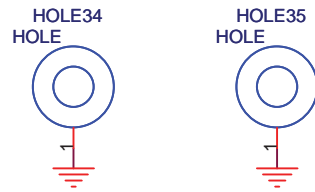
Size	Document Number	Rev
Custom	<b>iTPM &amp; RFID EEPROM</b>	
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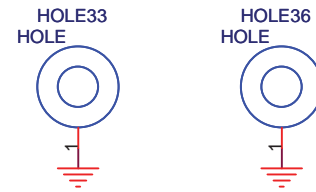
## MiniCard WLAN



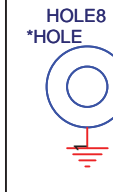
## MiniCard WWAN



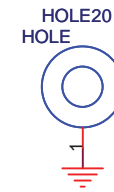
## Hole for PCH support



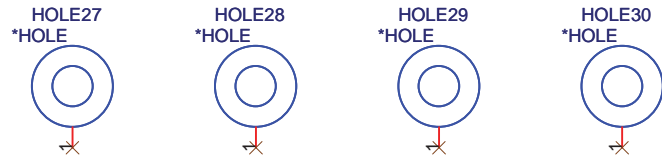
## Drink Hole



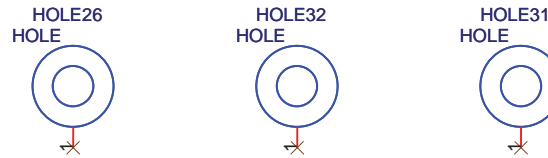
## Bluetooth nut



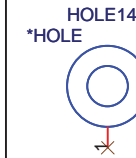
## Hole for CPU support



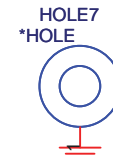
## VGA nut



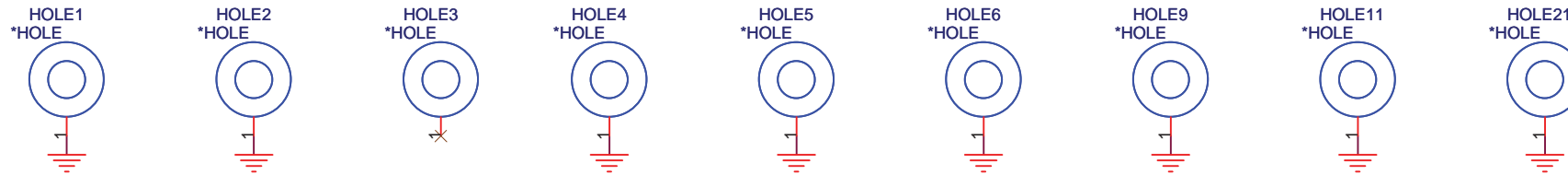
## Break Hole



## Boundary Hole (ODD)



## Boundary Hole

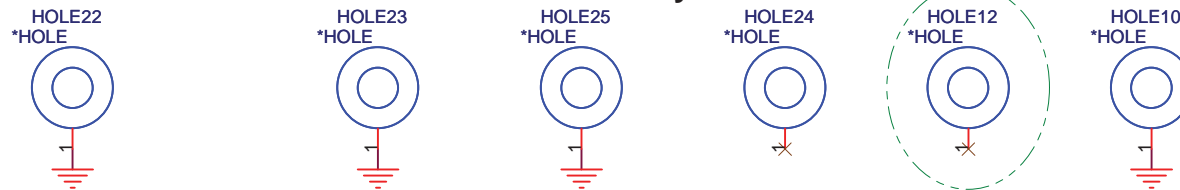


## ESD for ESATA

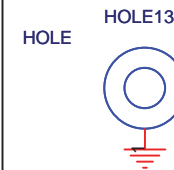


## Boundary Hole

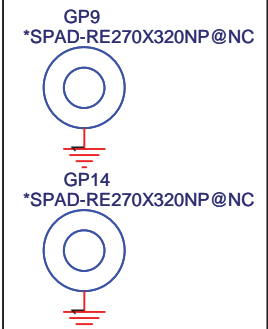
EC-C-13



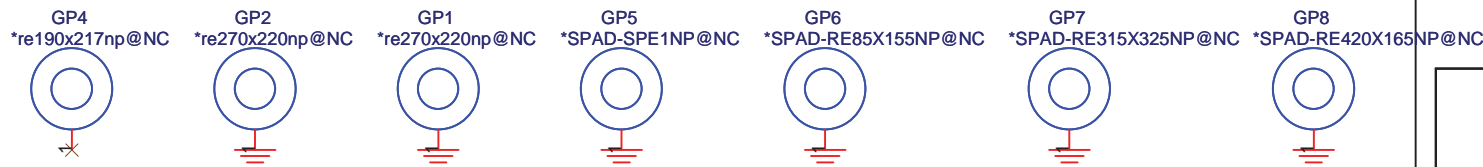
## KB Control/B NUT



## PAD



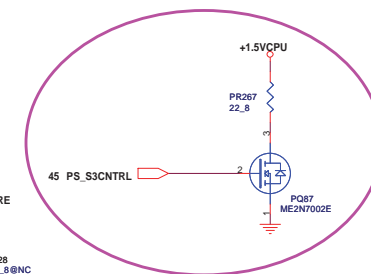
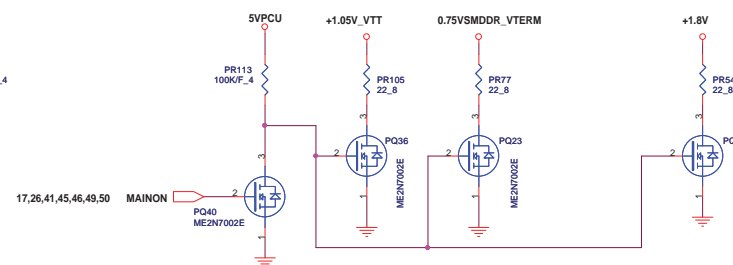
## PAD



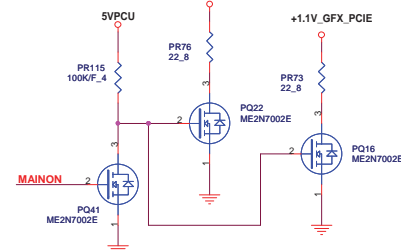
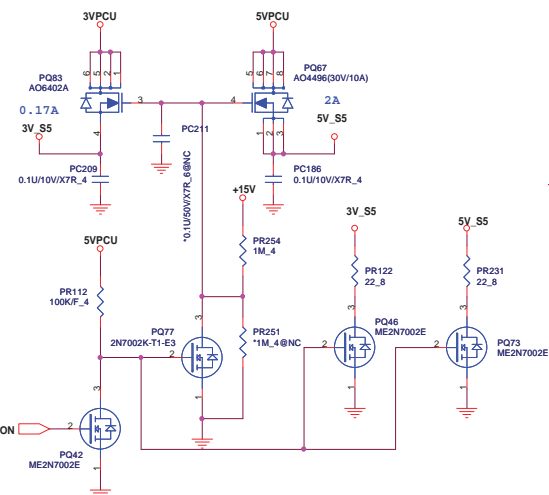
**LD-Note Calpella Discrete**  
**Quanta Computer Inc.**

Size Custom	Document Number	Rev
	<b>HOLE &amp; SKREW</b>	
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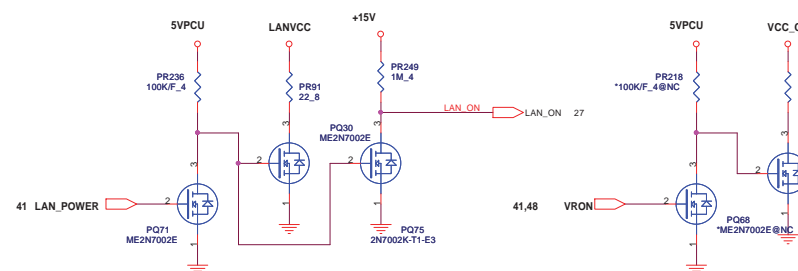
## +3V, +5V



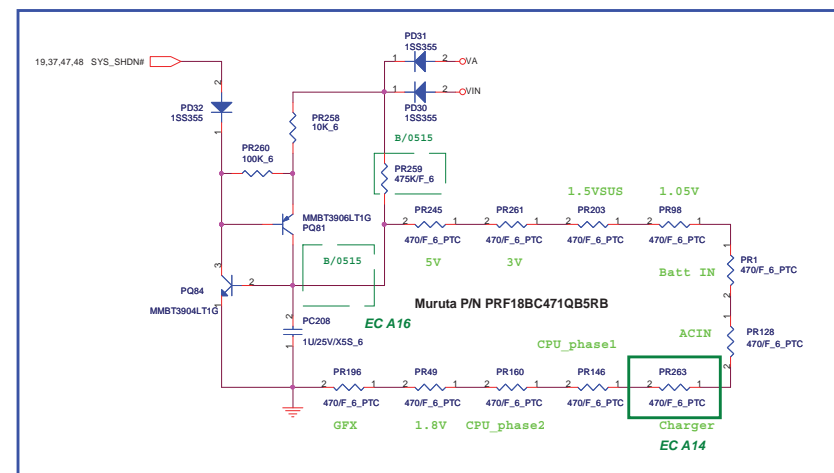
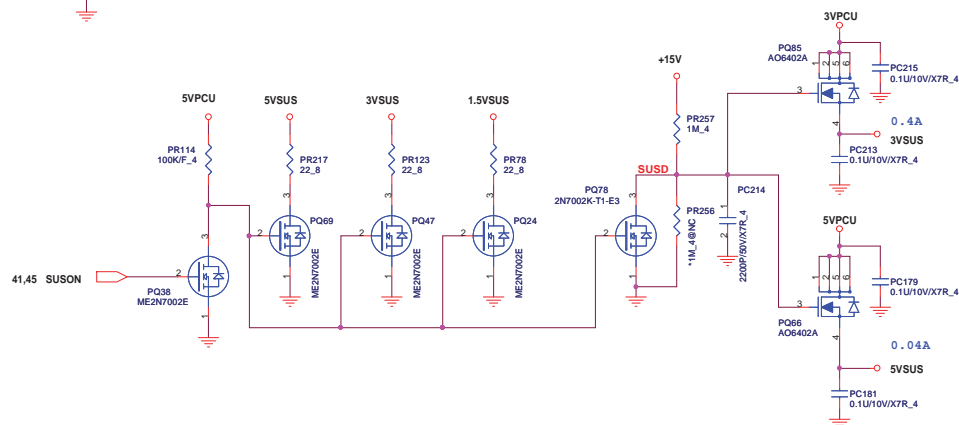
## +VCC\_GFX\_CORE

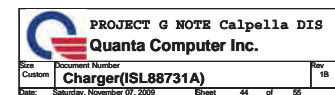


## LANVCC

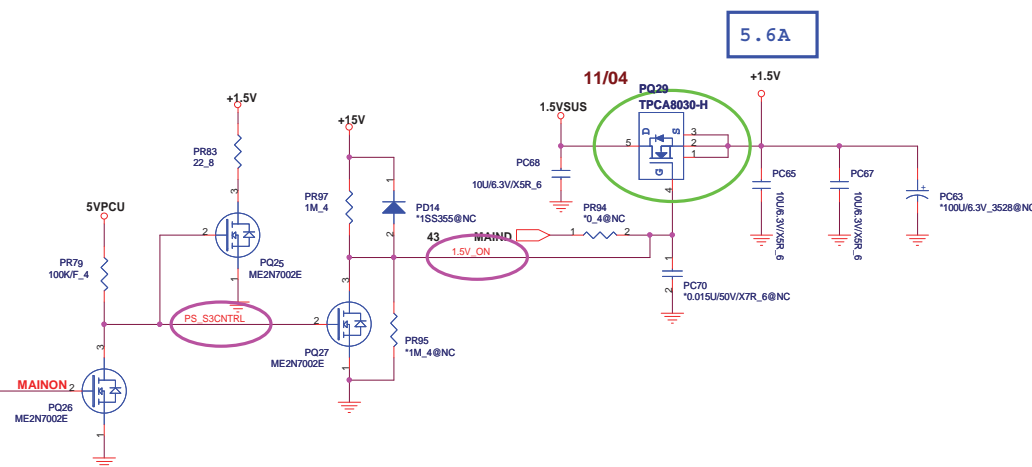
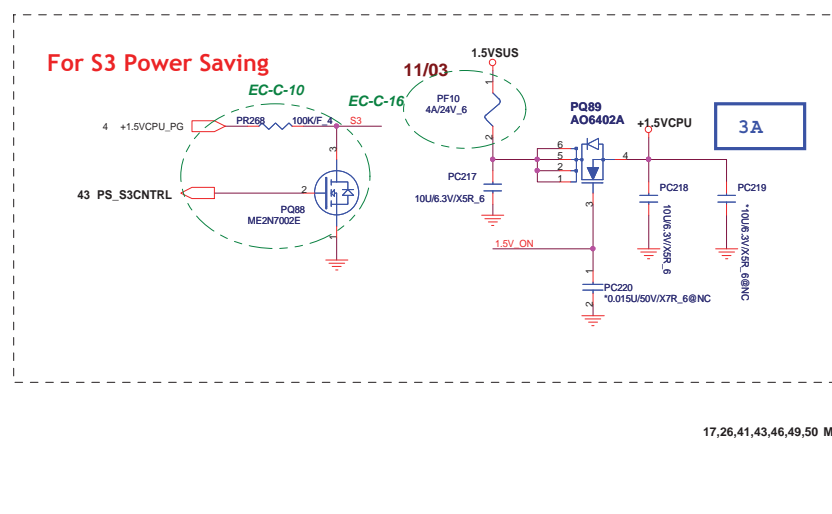
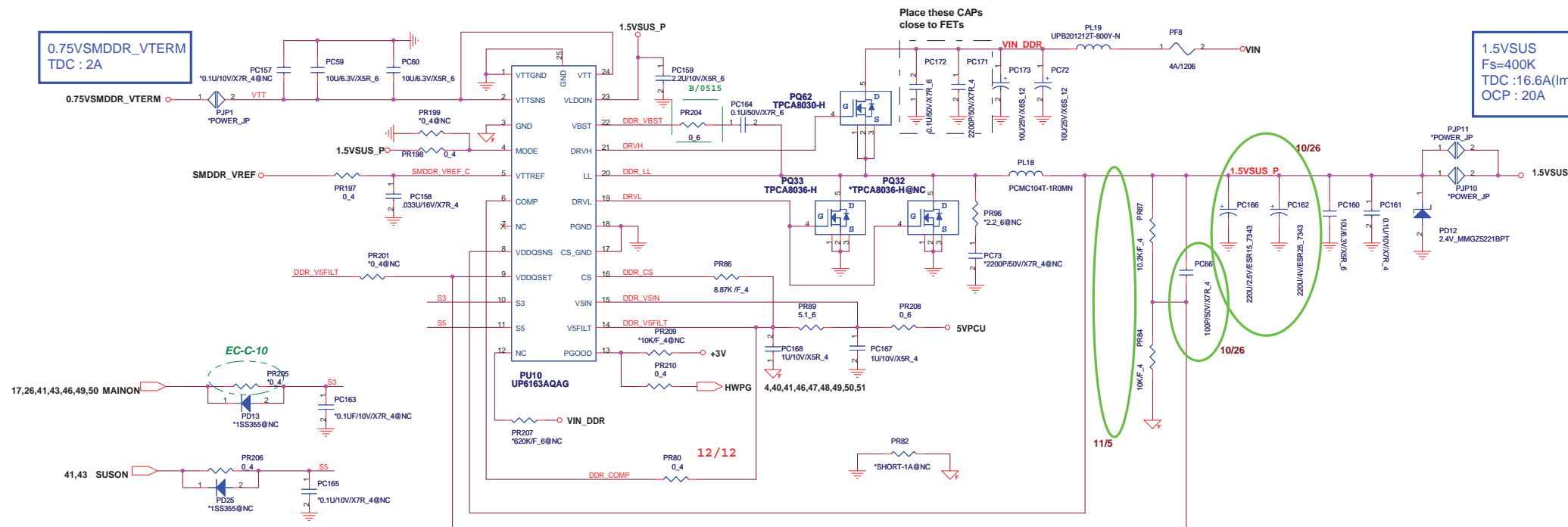


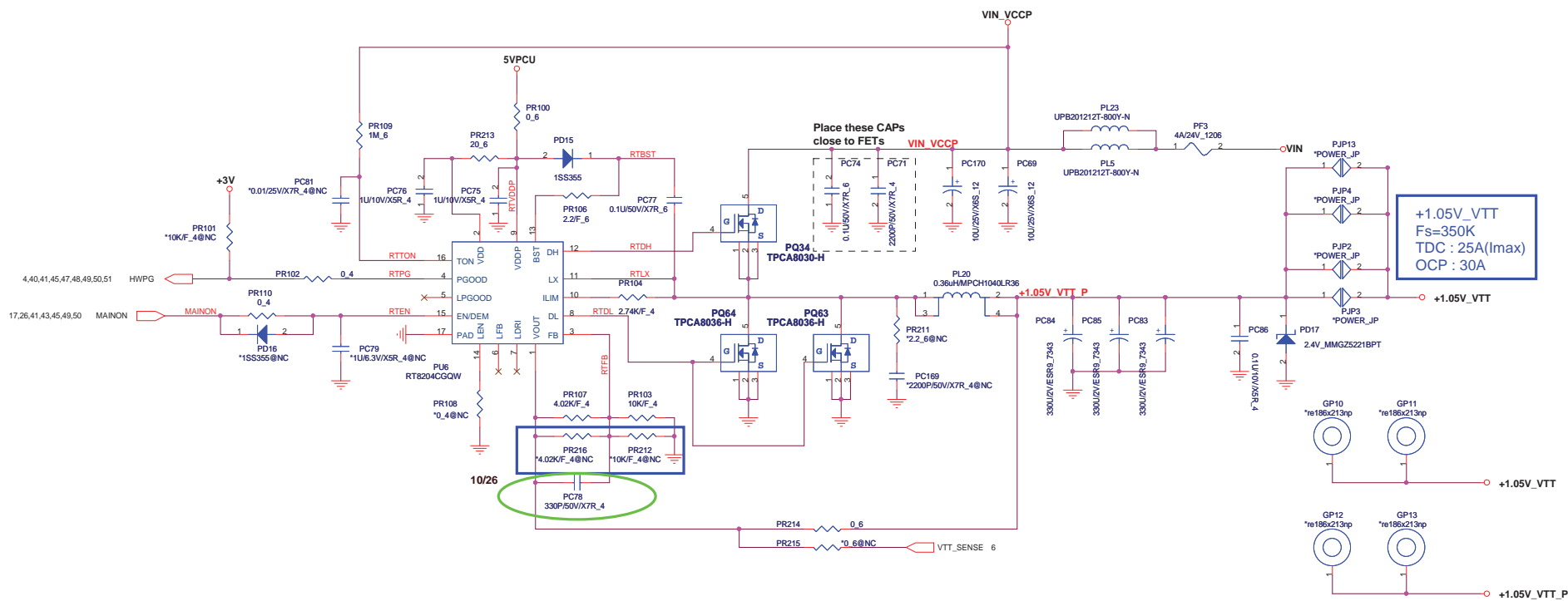
## 3VSUS, 5VSUS

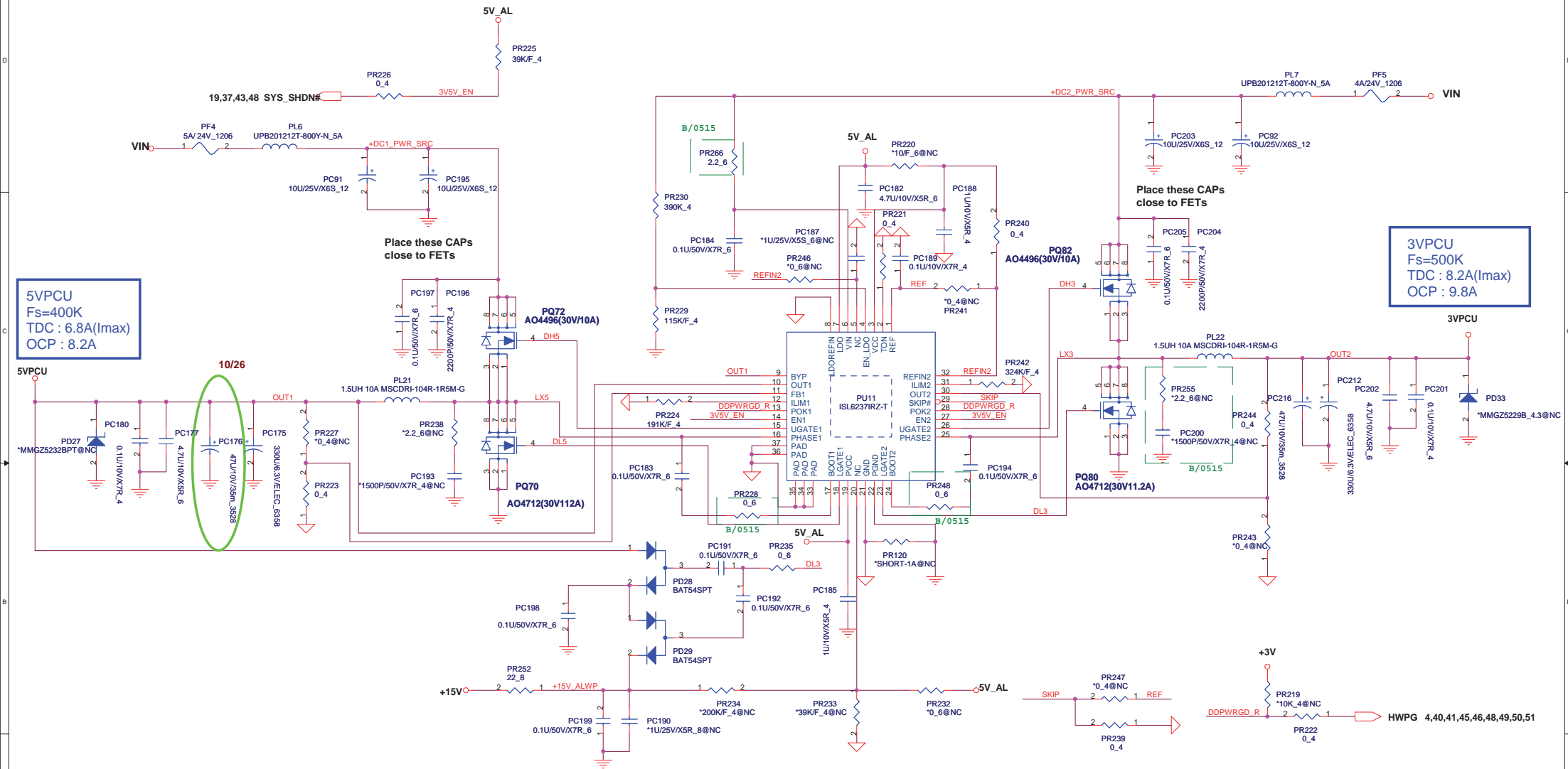




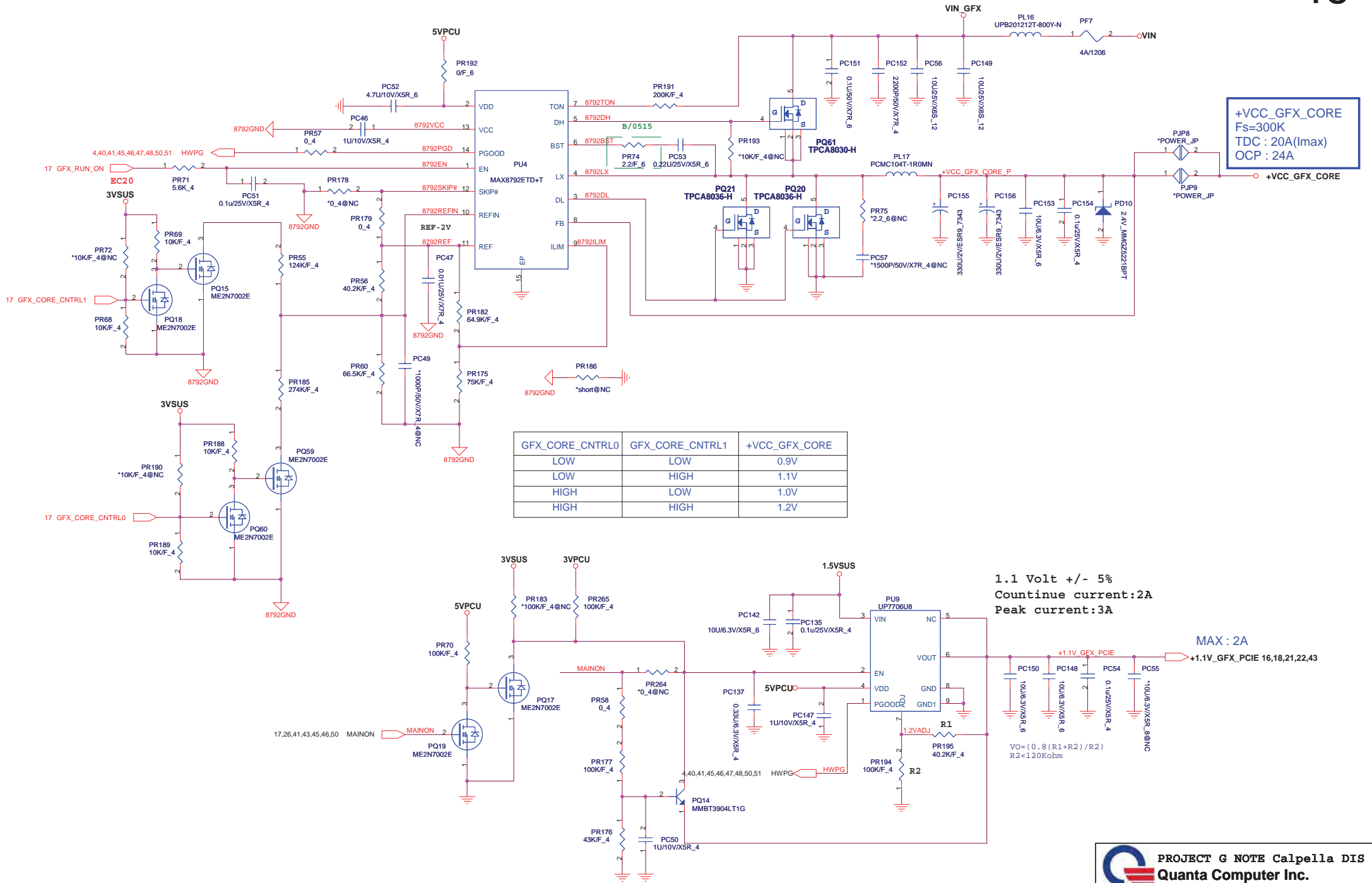


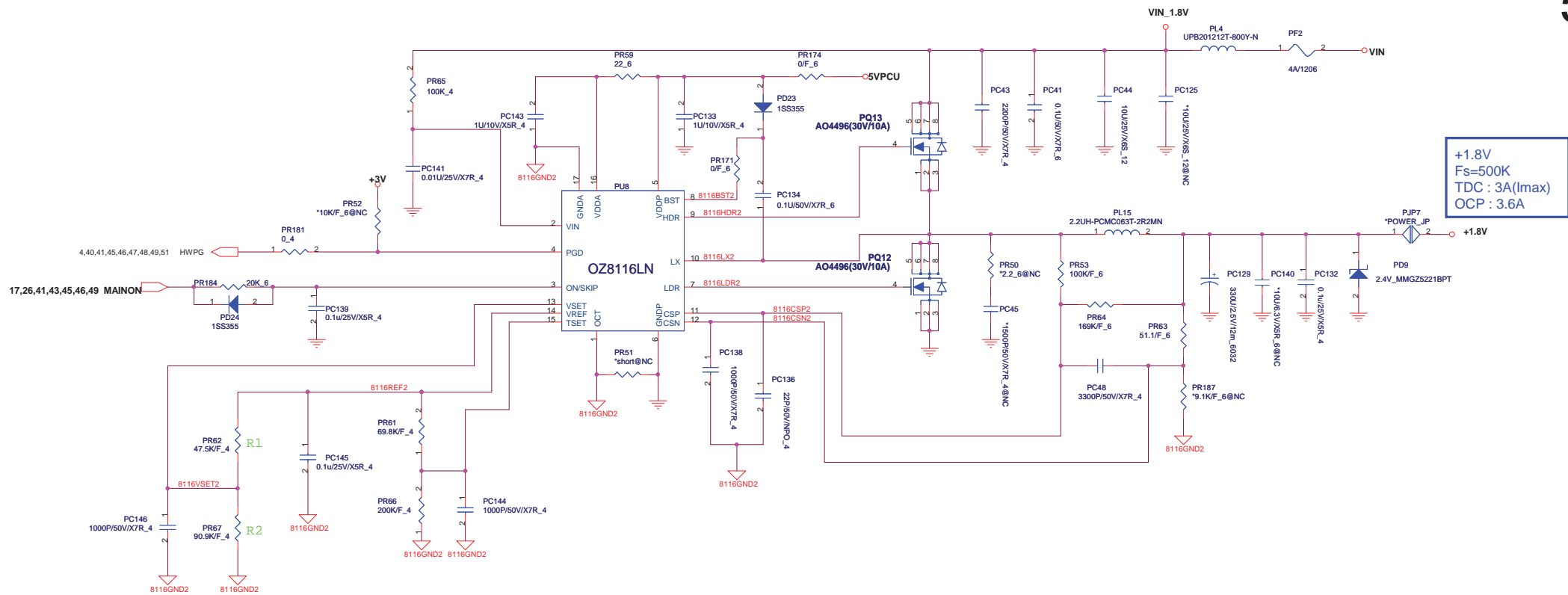




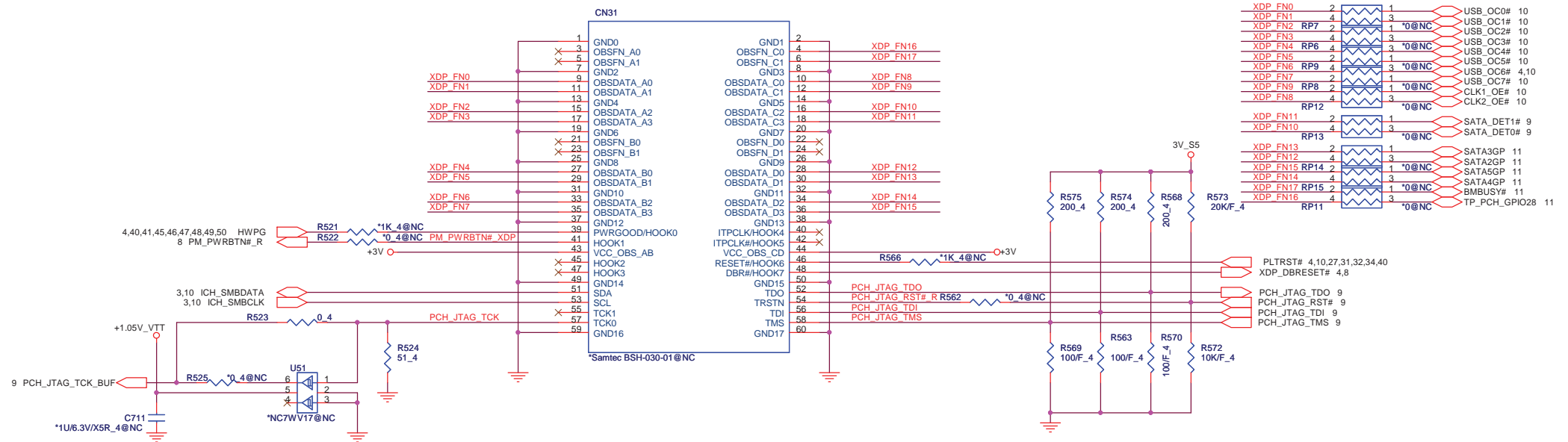
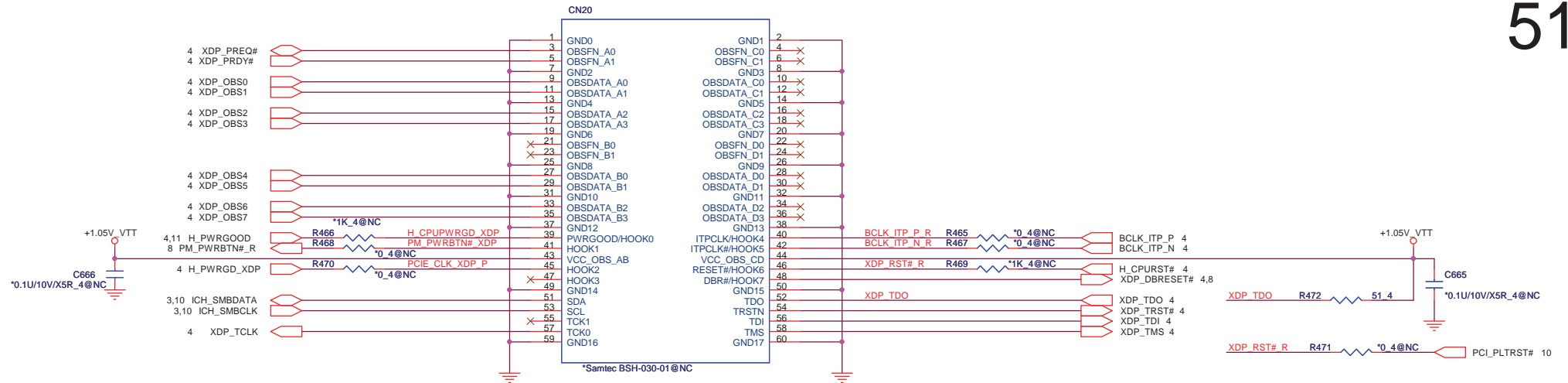












## Revision History

Revision	Date	Phase	Change List	Release Schematic Date	Release Gerber File Date
1A		DV	Initial release		

## Schematic Value Explanation Description :

### RESISTOR

Value	F	4	6	8	12	1210	*	Description
*1K/F_4	1%	0402 (1005 )					DE POP	1K ohm 1% SMD 0402 package and DE POP
1K_6	5%		0603 (1608 )				POP	1K ohm 5% SMD 0603 package and POP
1K_8	5%			0805 (2125 )			POP	1K ohm 5% SMD 0805 package and POP
1K_12	5%				1206 (3216 )		POP	1K ohm 5% SMD 1206 package and POP
1K_1210	5%					1210 (3225 )	POP	1K ohm 5% SMD 1210 package and POP

### CAPACITOR

Value	Voltage	Material	6				*	Description
*0.1U/10V/X5R_4	10V	X5R	0402 (1005 )				DE POP	0.1UF 10V X5R SMD 0402 package DE POP
1U/25V/X7R_6	25V	X7R	0603 (1608 )				POP	0.1UF 25V X7R SMD 0603 package POP

[illegible]

EC #	Page	Description	Part Affected
EC-C-01	10,33	Change card reader from port6 to port11	U58,U50
EC-C-02	26	Change Q52 from MOS to BJT to fix MIC function issue Change Q37 to MOS,Q38 to BJT to fix audio function issue Add LDO circuit to fix audio noise; un-stuff 0 ohm RES	Q52 Q37,Q38 U34,C498,C499,C500,L39
EC-C-04	40	Change PCH SPI BIOS power supplier from +3V to 3V_S5	U53
EC-C-05	8	Change PCIE_WAKE# pull high RES to 10K ohm	R508
EC-C-06	44	Add net MBATV from charger to EC	PR269,PR270
EC-C-07	25	ESD suggest to move C123	C123
EC-C-08	37	Un-stuff thermal IC MAX6694 relative circuits, suff G708 relative circuits	Un-stuff U42,Q32,Q35,Q42,Q24,C611,C608,C609,C610,C612,Q39,Q41,Q40,R432,R409 Stuff U41,C589,R394,R386,R387
EC-C-09	29 30 34	Stuff common choke to fix EMI issue	CML1 CML3,CML4 L23
EC-C-10	45	Solve S3 run in issue	Stuff PR268,PQ88 Un-stuff PR205
EC-C-11	29	Change black-berry charger IC to MAX14550AEETB+	U60
EC-C-12	29 41	Add enable pin AOU_USB_ON# to control black-berry charger function from EC Remove CELL-SET pin and two select RES	U35 U24,299,R300
EC-C-13	42	ME modify NON-PTH hole	HOLE12
EC-C-14	10	Reserve one 0 ohm RES to moniter leakage current in the future	R336
EC-C-15	26	Reserve codec Rev. VA @ VB option schematic	R628,R629,C762,R630,R347,R348
EC-C-16	45	Add one fuse to prevent from PQ89 burn out	PF10
EC-C-17	40	Change PCH BIOS to 4M Byte	U53